



GENERAL INFORMATION		
Item	Contents	Unit
LCD Size	3.5	Inch
Resolution	320RGB × 240dots	---
Pixel pitch (W × H)	0.219 × 0.219	mm
Active area (W × H)	70.08 × 52.56	mm
Viewing area (W×H)	70.68 × 53.16	mm
LCM outline (W×H×T)	76.90 × 63.90 × 4.54	mm
Display mode	IPS,NB	---
View angle(L/R/U/D)	80/80/80/80	Degree
TFT Driver IC	ST7272A	---
Interface Type	RGB 24bit	---
Color depth	16.7	M
LCM brightness	MIN 330	Cd/m ²
With/without tp	With RTP	---
TFT Power consumption	TBD	mw
BL Power consumption	TBD	mw
Operation Temperature	-20~70	°C
LED life time	30,000	Hrs

INTERFACE DESCRIPTION			
No.	SYMBOL	I/O	Description
1-2	LEDA		LED power anode.
3-4	LEDK		LED power cathode.
5	NC(YU)		NC(Touch panel Top Film Terminal).
6	NC(XR)		NC(Touch panel Right Glass Terminal).
7	NC		-
8	RES		This signal will reset the device and must be applied to properly initialize the chip. Signal is active low.
9	SPENA(nCS)		Display data/command selection pin in 4-line serial interface.
10	SCL		Serial communication clock input.
11	SDA		Serial communication data input.
12-19	B0-B7		Parallel 8-bit digital Blue data input.
20-27	G0-G7		Parallel 8-bit digital Green data input.
28-35	R0-R7		Parallel 8-bit digital Red data input.
36	HSYNC		Horizontal sync signal.
37	VSYNC		Vertical sync signal.
38	DCLK		Pixel clock input pin.
39-40	NC		-
41	VDDI		Power Supply for I/O(VDDI=3.3V).
42	VDD		Power Supply for Logic.
43	NC(YD)		NC(Touch panel Bottom Film Terminal).
44	NC(XL)		NC(Touch panel LEFT Glass Terminal).
45-51	NC		-
52	ENB(DEN)		Display enable pin from controller.
53	GND		System ground.
54	AVSS		Ground for digital circuits.

ELECTRICAL CHARACTERISTICS					
Item	Symbol	Min	Typ.	Max	Unit
Power supply voltage	VDD	3.0	3.3	3.6	V
Input signal high voltage	VIH	0.8VDD	---	VDD	V
Input signal low voltage	VIL	VSS	---	0.3VDD	V

BACKLIGHT DRIVING CONDITION					
Item	Symbol	Min	Typ.	Max	Unit
Forward current	If	--	20	--	mA
Forward voltage	Vf	16.8	19.2	20.4	V