# **PRODUCT SPECIFICATIONS**

For Customer: 
□ : APPROVAL FOR SPECIFICATION

Module No.: IE-SN-1316CH07R24-CB-1 Date : 2025.03.14

Version :A

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### For Customer's Acceptance:

Approved By	Comment

PREPARED	CHECKED	VERIFIED BY QA DEPT	VERIFIED BY R&D DEPT	

# 2. Revision Record

Date	Rev.No.	Page	Revision Items	Prepared
2025.03.19	А		The first release	

## 3. General Specifications

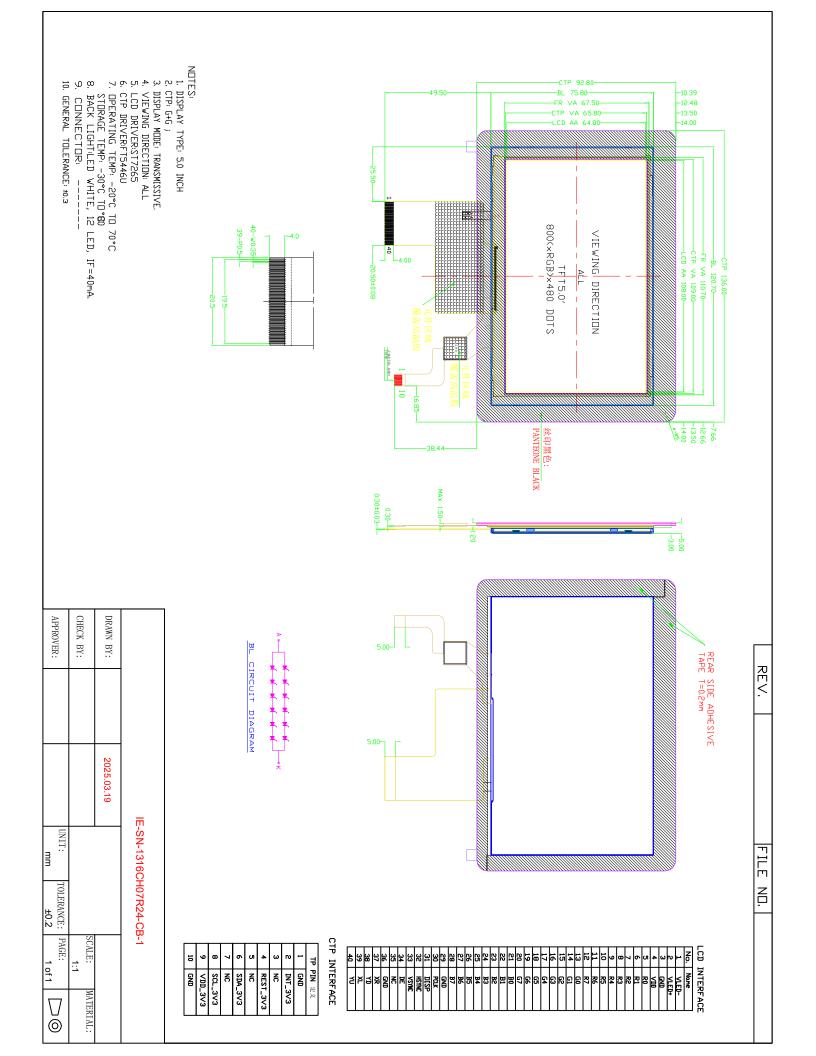
IE-SN-1316CH07R24-CB-1 is a TFT-LCD module. It is composed of a TFT-LCD

panel, driver IC, FPC, a back light unit. The  $5.0^{\prime\prime}$  display area contains  $800 \times 480$  pixels and can display up to 16.7M colors. This product accords with RoHS environmental criterion.

Item	Contents	Unit	Note
LCD Type	TFT	-	
Display color	16.7M		1
Viewing Direction	ALL	O'Clock	
Operating temperature	-20~+70	C	
Storage temperature	-30~+80	°	
Module size	120.70(W)×75.80(H)×3.0/4.2(T)	mm	2
Active Area(W×H)	108×64.8	mm	
Number of Dots	800×RGB×480	dots	
Drive IC	ST7262	-	
Power Supply Voltage	3.3	V	
Backlight	18-LEDs (white)	pcs	
Data Transfer	RGB 24-bit	-	
Weight		g	

Note 1: Color tune is slightly changed by temperature and driving voltage.

Note 2: Without FPC and Solder.



## 5. Absolute Maximum Ratings(Ta=25℃)

Item	Symbol	Min.	Max.	Unit	Note
Power Supply Voltage	VDD	-0.3	3.6	V	
Input Voltage	V <sub>In</sub>	-0.3	VDD+0.5	V	1,2
Current of LED	ILED	0	60	mA	

## 5.1 Electrical Absolute Maximum Ratings.(Vss=0V ,Ta=25℃)

Notes:

1. If the module is above these absolute maximum ratings. It may become permanently damaged.

Using the module within the following electrical characteristic conditions are also exceeded, the module will malfunction and cause poor reliability.

- 2. VDD>V<sub>SS</sub> must be maintained.
- 3. Please be sure users are grounded when handing LCD Module.

### 5.2 Environmental Absolute Maximum Ratings.

Item	Stor	age	Opera	Note	
	MIN.	MAX.	MIN.	MAX.	
Ambient Temperature	-30℃	30℃	-20℃	<b>70℃</b>	1,2
Humidity	-	-	-	-	3

- 1. The response time will become lower when operated at low temperature.
- 2. Background color changes slightly depending on ambient temperature.

The phenomenon is reversible.

3. Ta<=40°℃:85%RH MAX.

Ta>=40  $^{\circ}$ C:Absolute humidity must be lower than the humidity of 85%RH at 40  $^{\circ}$ C.

# 6. Electrical Specifications and Timing Characteristics

Parameter		Symbol	Condition	Min	Тур	Max	Unit	Note
Power supply		VDD	Ta=25℃	3.0	3.3	3.6	V	
Input	'H'	VIH	VDD=3.3V	0.7VDD	-	VDD	V	
voltage	'L'	VIL	VDD=3.3V	0	-	0.3VDD	V	
Curren	Current		Normal mode	-	-	-	mA	2
Consump	otion	I <sub>CC2</sub>	Sleep mode	-	0.03	0.09	mA	2

## 6.1 Electrical characteristics(Vss=0V ,Ta=25℃)

Note:

1:When an optimum contrast is obtained in transmissive mode.

2: Tested in  $1 \times 1$  chessboard pattern.

## 6.2 LED backlight specification(VSS=0V ,Ta=25℃)

Item		Symbol	Condition	Min	Тур	Max	Unit	Note
Supply voltage		-	-	-	19.2	-	V	1
Supply current		l <sub>f</sub>	-	-	60	-	mA	2
Forward	Forward Normal Ipn		6-chip	-	60	-		
current	Dimming	I <sub>pd</sub>	series x 3	-	-	-	mA	

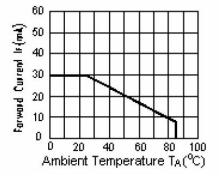
Note:

1: VLED=VLED(+)-VLED(-).

2:The current of LED is 20mA.

A LED drive in constant current mode is recommended.

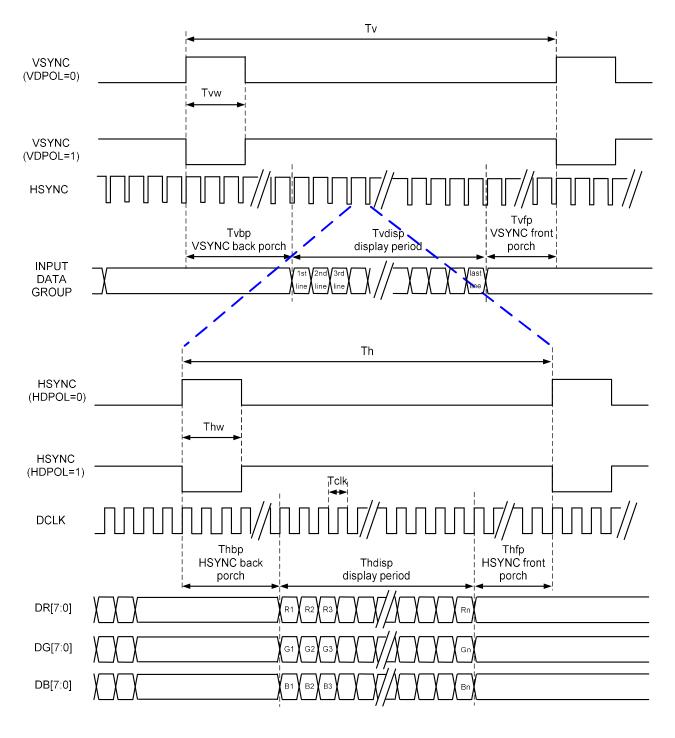
3: LED power consumption is around 0.768W.



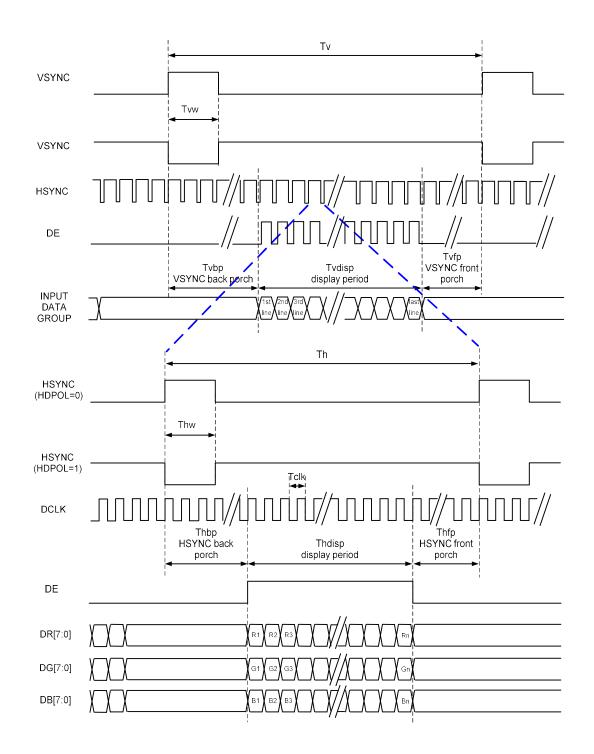
# 6.3 Interface signals

Pin	Symbol	Description.
1	LED_K	LED back light(Cathode)
2	LED_A	LED back light(Anode)
3	GND	GND
4	VDD	Power supply
5~12	R0-R7	Red data bus
13~20	G0-G7	Green data bus
21~28	B0-B7	Blue data bus
29	GND	GND
30	PCLK	Data clock
31	DISP	Standby mode select pin
32	HSYNC	Line SYNC signal
33	VSYNC	Frame SYNC signal
34	DE	Data Enable Input
35	NC	NC
36	GND	GND
37	X1/NC	Touch Panel Control Pin or NC
38	Y1/NC	Touch Panel Control Pin or NC
39	X2/NC	Touch Panel Control Pin or NC
40	Y2/NC	Touch Panel Control Pin or NC

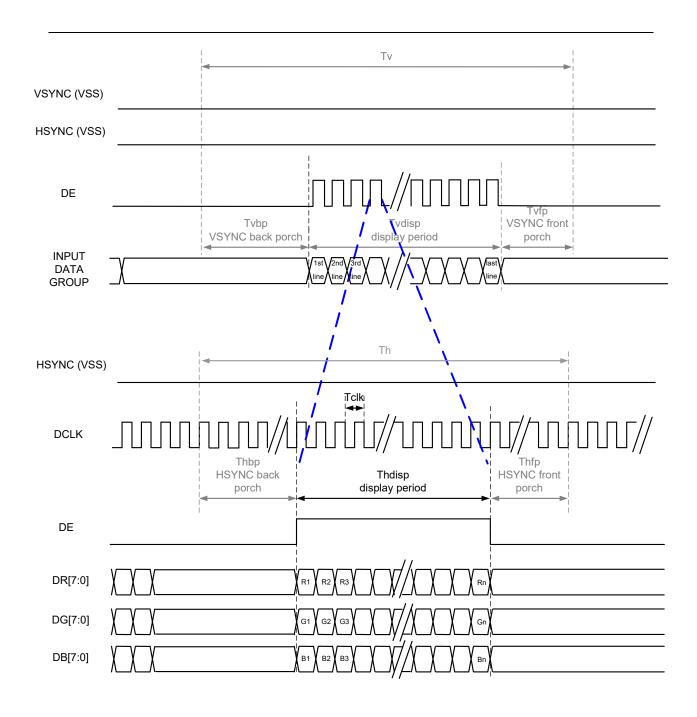
# 6.4 Timing Characteristics



SYNC Mode



SYNC-DE Mode



**DE Mode** 

	Parallel 24-bit RGB Interface Timing Table									
	ltem	Symbol	Min.	Тур.	Max.	Unit	Remark			
DCLK	(Frequency	Fclk	23	25	27	MHz				
	Period Time	Th	808	816	896	DCLK				
	Display Period	Thdisp		800		DCLK				
HSYNC	Back Porch	Thbp	4	8	48	DCLK				
	Front Porch	Thfp	4	8	48	DCLK				
	Pulse Width	Thw	2	4	8	DCLK				
	Period Time	Τv	492	496	504	HSYNC				
	Display Period	Tvdisp		480		HSYNC				
VSYNC	Back Porch	Tvbp	6	8	12	HSYNC				
	Front Porch	Tvfp	6	8	12	HSYNC				
	Pulse Width	Tvw	2	4	8	HSYNC				

Parallel 24-bit RGB Input Timing (PVDD=PVDD1=VDD=VDDI= 3.3V, AGND= 0V, TA=25°C)

Note: 1. The minimum blanking time depends on the GIP timing of the panel specification

2. To ensure the compatibility of different panels, it is recommended to use the typical setting.

3. It is necessary to keep Tvbp =12 and Thbp =43 in sync mode. DE mode is unnecessary to keep it.

## 7. Optical Characteristics

Item	Sy	mbol	Condition	Min.	Тур.	Max.	Unit	Note
Brightness	E	Зр	<i>θ</i> =0°	500	520	550	Cd/m <sup>2</sup>	1
Uniformity	2	lBp	Φ <b>=</b> 0°	70	80	-	%	1,2
	3	:00		-	80	-		
Viewing	6	:00	Cr≥10	-	80	-	Deg	3
Angle	9	:00		-	80	-	Deg	5
	12	2:00		-	80	-		
Contrast Ratio		Cr	<i>θ</i> =0°	-	1500		-	4
Response Time	Т	r+T <sub>f</sub>	Ф <b>=</b> 0°		30		ms	5
		х		0.26	0.31	0.36	-	
	W	у		0.28	0.33	0.38	-	-
		Y		-	-	-		
		Х					-	
Color of	R	у					-	
		Y		-	-	-		
Coordinate		X	<i>θ</i> =0°				-	1,6
	G	у	Ф <b>=0°</b>				-	,
		Y		-	-	-		
		X					-	-
	В	у					-	
		Y		-	-	-		
NTSC Ratio	S			-	60	-	%	

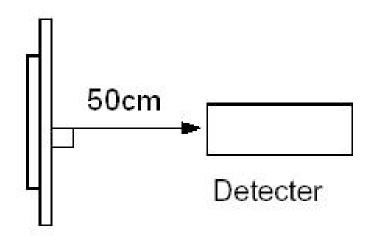
Note: The parameter is slightly changed by temperature, driving voltage and materiel

Note 1: The data are measured after LEDs are turned on for 5 minutes. LCM displays full white. The brightness is the average value of 9 measured spots. Measurement equipment PR-705 (Φ8mm)

Measuring condition:

- Measuring surroundings: Dark room.
- Measuring temperature: Ta=25℃.
- Adjust operating voltage to get optimum contrast at the center of the display.

Measured value at the center point of LCD panel after more than 5 minutes while backlight turning on.

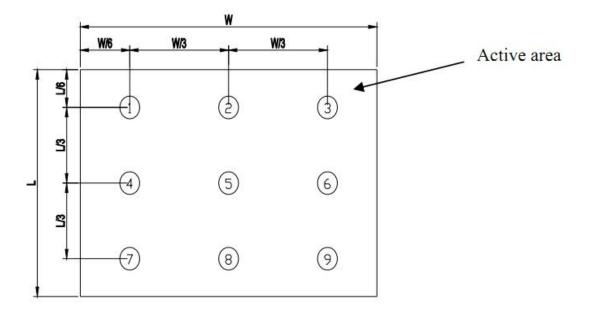


Note 2: The luminance uniformity is calculated by using following formula.

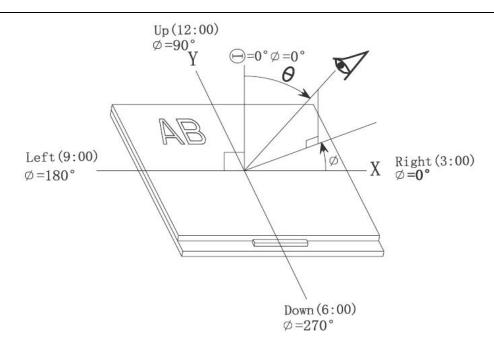
⊿Bp = Bp (Min.) / Bp (Max.)×100 (%)

Bp (Max.) = Maximum brightness in 9 measured spots

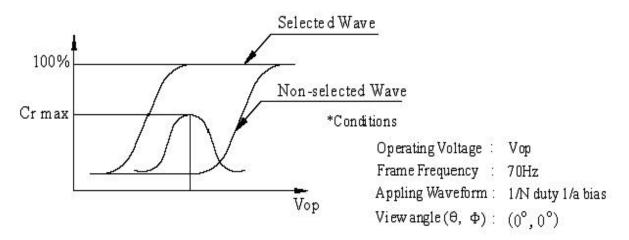
Bp (Min.) = Minimum brightness in 9 measured spots.



Note 3: The definition of viewing angle: Refer to the graph below marked by  $\theta$  and  $\phi$ 



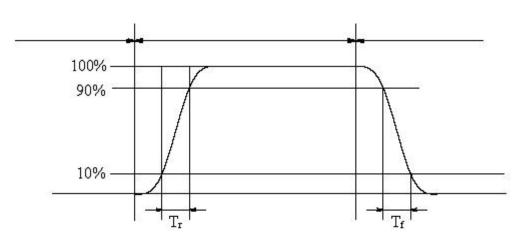
Note 4: Definition of contrast ratio.(Test LCD using DMS501)



Contrast ratio(Cr) =  $\frac{Brightness \ of \ selected \ dots}{Brightness \ of \ non-selected \ dots}$ 

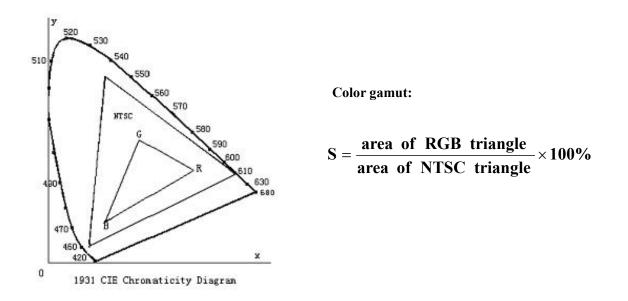
Note 5: Definition of Response time. (Test LCD using DMS501):

The output signals of photo detector are measured when the input signals are changed from "black" to "white" (falling time) and from "white" to "black" (rising time), respectively. The response time is defined as the time interval between the 10% and 90% of amplitudes.Refer to figure as below.



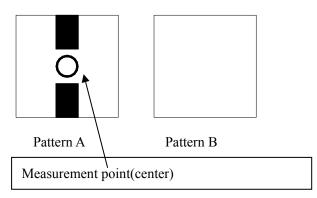
The definition of response time





Note 7: Definition of cross talk.

Cross talk ratio(%)=|pattern A Brightness-pattern B Brightness|/pattern A Brightness\*100



Electric volume value=3F+/-3Hex

# 8. Reliability Test Items and Criteria

No	Test Item	Test condition	Criterion
1	High Temperature Storage	80℃±2℃ 96H Restore 2H at 25℃ Power off	
2	Low Temperature Storage	-30℃±2℃ 96H Restore 2H at 25℃ Power off	
3	High Temperature Operation	70℃±2℃ 96H Restore 2H at 25℃ Power on	<ul> <li>1. After testing,</li> <li>cosmetic and electrical</li> <li>defects should not</li> </ul>
4	Low Temperature Operation	-20℃±2℃ 96H Restore 4H at 25℃ Power on	<ul> <li>happen.</li> <li>2. Total current</li> <li>consumption should</li> <li>not be more than twice</li> </ul>
5	High Temperature/Humidity Operation	50℃±2℃ 90%RH 96H Power on	of initial value.
6	Temperature Cycle(Storage)	-20°C ←-25°C→70°C 30min 5min 30min after 5 cycle, Restore 2H at 25°C Power off	
7	Vibration Test	10Hz~150Hz, 100m/s², 120min	Not allowed cosmetic
8	Shock Test	Half- sine wave,300m/s <sup>2</sup> ,11ms	and electrical defects.
9	ESD Test	Air discharge:+/-8KV, Contact discharge:4KV	

Note: Operation: Supply 3.3V for logic system.

The inspection terms after reliability test, as below

ITEM	Inspection			
Contrast	CR>50%			
IDD	IDD<200%			
Brightness	Brightness>60%			
Color Tone	Color Tone+/-0,05			

## 9 Quality level

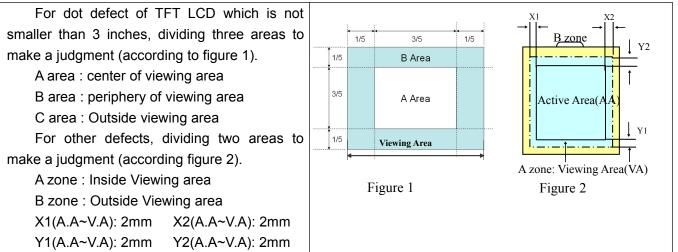
### 9.1 Classification of defects

Major defects (MA): A major defect refers to a defect that may substantially

degrade usability for product applications, including all functional defects(such as no display, abnormal display, open or missing segment, short circuit, missing component), outline dimension beyond the drawing, progressive defects and those affecting reliability.

Minor defects (MI): A minor defect refers to a defect which is not considered to be able to substantially degrade the product application or a defect that deviates from existing standards almost unrelated to the effective use of the product or its operation, such as black spot, white spot, bright spot, pinhole, black line, white line, contrast variation, glass defect, polarizer defect, etc.

### 9.2 Definition of inspection range



#### 9.3 Inspection items and general notes

	5.5 inspection items and general notes				
General notes	<ul> <li>1.Should any defects which are not specified in this standard happen, additional standard shall be determined by mutual agreement between customer and TIANMA.</li> <li>2.Viewing area should be the area which TIANMA guarantees.</li> <li>3.Limit sample should be prior to this Inspection standard.</li> <li>4.Viewing judgment should be under static pattern.</li> <li>5.Inspection conditions <ul> <li>Inspection distance: 250 mm (from the sample)</li> <li>Temperature : 25±5 °C</li> <li>Inspection angle : 45 degrees in 6 o'clock direction (all defects in viewing area should be inspected from this direction)</li> </ul> </li> </ul>				
	Pinhole, Bright spot, Black spot, White spot, Black line, White Line, Foreign particle, Bubble	The color of a small area is different from the remainder. The phenomenon doesn't change with voltage			
	Contrast variation	The color of a small area is different from the remainder. The phenomenon changes with voltage			
Inspection items	Polarizer defect	Scratch, Dirt, Particle, Bubble on polarizer or betweer polarizer and glass			
	Dot defect (TFT LCD)	The pixel appears bright or dark abnormally when display			
	Functional defect	No display, Abnormal display, Open or missing segment, Short circuit, False viewing direction			

Glass defect	Glass crack, Shaved corner of glass, Surplus glass
PCB defect	Components assembly defect

# 9.4 Outgoing Inspection level

Outgoing Inspection	Inspection conditions	Inspection				
standard		Min.	Max.	Unit	IL	AQL
Major Defects	Major Defects See 8.3 general notes		See 8.5		II	0.065
Minor Defects See 8.3 general notes		S	See 8.	5	II	0.065
Note: Sampling standard conforms to GB2828						

# 9.5 Inspection Items and Criteria

Inspection items		Judgment standard				
		Category		Acceptable number		
			Category		A zone	B zone
			Α	Φ<=0.20	Neglected	Neglected
	Black spot, White	a	В	0.20<Ф<=0.25	3	Neglected
	spot, Pinhole, Foreign		С	0.25<Ф<=0.3	2	Neglected
1	Particle, Particle in or on glass,	$\Phi = (a+b)/2(mm)$	D	0.3<Ф<=0.4	1	3
	Scratch on glass		Е	0.4<Ф<=0.5	0	2
		(a/b<2.5)		tal defective point(B,C)	1	-
			A	W<=0.03	Neglected	Neglected
		line, and Particle Between Polarizer and glass, Scratch on	В	0.03 <w<=0.05 L&lt;=3.0</w<=0.05 	3	Neglected
	Black line, White line, and Particle Between		с	0.05 <w<=0.1 L&lt;=3.0</w<=0.1 	2	Neglected
2			D	0.05 <w<=0.1 L&lt;=4.0</w<=0.1 	1	3
	<u></u>		E	W>0.1 L>4.0	0	2
			То	tal defective point(B,C)	1	-

3	Bright spot		any size		none	none	
	Contrast variation		A	Φ<0.2	Neglected		
			В	0.2<Ф<=0.3	2		
4		b	С	0.3<Ф<=0.4	1	<ul> <li>Neglected</li> </ul>	
		$a \\ \Phi = (a+b)/2(mm)$	D	0.4<Ф	0		
				tal defective point(B,C)	3		
5	Bubble inside cell			any size	none	none	
	Polarizer defect	Scratch ,damage on polarizer, Particle on polarizer or between polarizer and glass.	Refer to item 1 and item 2.				
6	(if Polarizer is used)	Bubble, dent and convex	A	Ф<=0.1	Neglected	Neglected	
	,		В	0.1 <Ф<=0.2	2	Neglected	
			С	0.2 <Ф<=0.3	1	2	
7	Surplus glass	Stage surplus glass	B<=0.3mm				
glass		ould not influence outline	dimension and as	ssembling.			
8	Open segment or o	open common	Not permitted				
9	Short circuit		Not permitted				
10	False viewing direction		Not permitted				
11	Contrast ratio uneven		According to the limit specimen				
12	Crosstalk		According to the limit specimen				
13	Black /White spot(display)			Refer to item 1			
14	Black /White line(display)		Refer to item 2				

		ludement standard			
Inspection items					
	i )The front of lead terminals	A a≤ t, b≤1/5W, c≤3mm			
	w a c	B Crack at two sides of lead terminals should not cover patterns and alignment mark			
Glass	ii )Surrounding $\frac{t}{\text{seal}}$ ck-non-contact side	b < Inner borderline of the seal Max.3	Max.3		
defect crack	iii ) Surroundin <sub>seal</sub> ack- contact side t c b a <u>Inner border line of the seal</u> Outer border line of the seal				
	iv )Corner	A a <= t, b <= 3.0, c <= 3.0			
	w b c	B Glass crack should not cover patterns u and alignment mark and patterns.			
	defect	Glass defect crack Glass w b uner border line of the seal Outer border line of the seal Outer border line of the seal Outer border line of the seal Unit border line of the seal Outer border line of the seal	i) The front of lead terminals     A     a ≤ t, b ≤1/5W, c ≤3mm       ii) The front of lead terminals     B     Crack at two sides of lead terminals should not cover patterns and alignment mark       ii) Surrounding the contract side     b     Inner border line of the seal       iii) Surroundin seal tok- contact side     b     Inner border line of the seal       iii) Surroundin seal tok- contact side     b     Inner border line of the seal       iii) Surroundin seal tok- contact side     b     A       iii) Surroundin seal tok- contact side     b     Conter borderline of the seal       iii) Surroundin seal tok- contact side     b     Conter borderline of the seal       iii) Surroundin seal tok- contact side     b     Conter borderline of the seal       iv) Corner     A     a <= t, b <= 3.0, c <= 3.0		

			Judgment standard
		Inspection items	Category(application: B zone)
		Component soldering: No cold soldering, short, open circuit, burr, tin ball The flat encapsulation component position deviation must be less than 1/3 width of the pin (Pic.1); the sheet component deviation: Pin deviates from the pad and contact with the near components is not permitted (Pic.2) lead defect: The lead lack must be less than 1/3 of its width; The lead burr must be less than 1/3 of the seam; Impurities connect with the near leads is not permitted	Component $L \leq W/2$ W Soldering pad Lead $L \geq 0$ Component $L \geq 0$ L1>0
16	PCB defect	Connector soldering: Soldering tin is at contact position of the plug and socket is not permitted No foundation is scald Serious cave distortion on plug and socket contact pin is not permitted	head Base Board Soldering tin is not permit in this area Soldering tin is not permit in this area
		Glue on root of the speaker receiver and motor lead: The insulative coat of the lead must join into the PCB; the protected glue must envelop to the insulative coat.	Glue PCB Insulative coat

## **10. Precautions for Use of LCD Modules**

## **10.1 Handling Precautions**

- 10.1.1 The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.
- 10.1.2 If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.
- 10.1.3 Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.
- 10.1.4 The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.
- 10.1.5 If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:
  - Isopropyl alcohol
  - Ethyl alcohol
  - Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:
    - Water
    - Ketone
    - Aromatic solvents
- 10.1.6 Do not attempt to disassemble the LCD Module.
- 10.1.7 If the logic circuit power is off, do not apply the input signals.
- 10.1.8 To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
  - a. Be sure to ground the body when handling the LCD Modules.
  - b. Tools required for assembly, such as soldering irons, must be properly ground.
  - c. To reduce the amount of static electricity generated, do not conduct

assembly and other work under dry conditions.

d. The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

#### **10.2 Storage precautions**

- 10.2.1 When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.
- 10.2.2 The LCD modules should be stored under the storage temperature range. If the LCD modules will be stored for a long time, the recommend condition is:

Temperature :  $0^{\circ} C \sim 40^{\circ} C$ 

Relatively humidity: ≤80%

10.2.3 The LCD modules should be stored in the room without acid, alkali and harmful gas.

10.3 The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.