MODEL NO. : IE-G-1819CH05MP-CB-1

ISSUED DATE: 2020-03-04

VERSION : Ver 1.0

☐Preliminary Specification
■Final Product Specification

CUSTOMER:

Approved by	Notes

REVISION RECORD

Rev. NO.	Rev. Date	Description	Remarks
1.0	2020-03-04	First Release	

TABLE OF CONTENT

1. General Specifications

IE-G-1819CH05MP-CB-1 is a color active matrix LCD module incorporating amorphous silicon TFT (Thin Film Transistor). It is composed of a color TFT-LCD panel, driver IC, FPC， and a back light unit. The module display area contains 1024x 600 pixels. This product accords with RoHS environmental criterion.

Item	Display Panel	Remark
Size	7.0 inch	/
Display Mode	Normally Black, Transmissive LCD	/
Viewing Direction	Free View	/
Module area(W x H x T)	180.0 x 109.0 x 5.3	mm
TFT area(W x H x T)	164.9 x 100.0 x 3.5	mm
Active Area(W x H)	154.2144 x 85.92	mm
Number of Dots	1024RGB x 600	/
Pixel Pitch(W x H)	0.1506 x 0.1432	mm
Surface treatment	Anti-Glare	/
Interface Type	4 Lanes MIPI	/
Backlight Type	27 LED(3 Serial*9)	/
Driver IC	EK79007AD2+EK73217BCGA	
Weight	TBD	/

2. ABSOLUTE MAXIMUM RATINGS

ITEM	Symbol	Min	Max	Uint	Note
Digital Supply Voltage	VDD	-0.3	2.0	V	
Analog Supply Voltage	AVDD	-0.5	15.0	V	
Gate On Voltage	VGH	-0.3	40.0	V	
Gate Off Voltage	VGL	-20	0.3	V	
Gate On - Gate Off Voltage	VDDG-VEEG	-	40	V	
Operation Temperature	Top	-10	60	℃	Note 1
Storage Temperature	TSgt	-20	70	℃	Note 1

3. ELECTRICAL CHARACTERISTICS

3.1 Operating Conditions

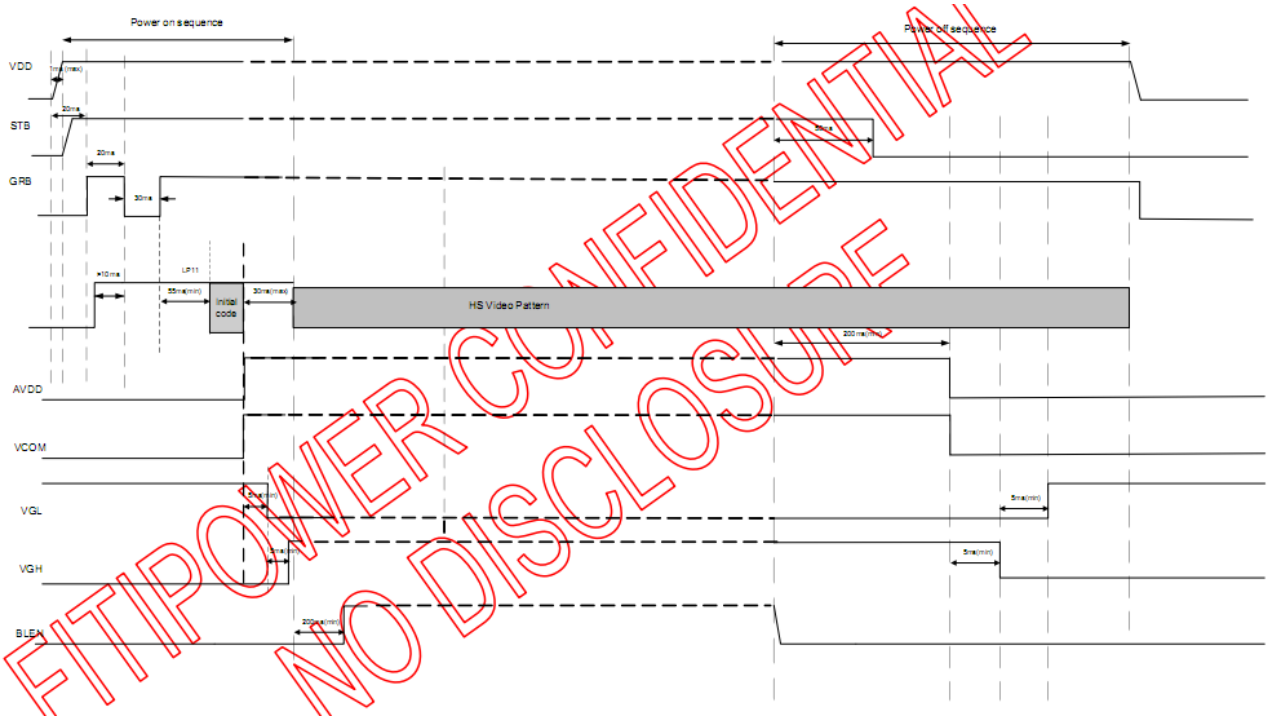
ITEM	Symbol	Min	TYP	Max	Uint	Note
Digital Supply Voltage	VDD	1.71	1.8	1.89	V	
Analog Supply Voltage	AVDD	9.4	9.6	9.8	V	
Gate On Voltage	VGH	17	18.0	19.0	V	
Gate Off Voltage	VGL	-7.0	-6.0	-5.0	V	
Common Electrode Driving Signal	VCOM	3.1	3.3	3.5	V	

Note1：Please adjust VCOM to make the flicker level be minimum.

3.2 Current Consumption

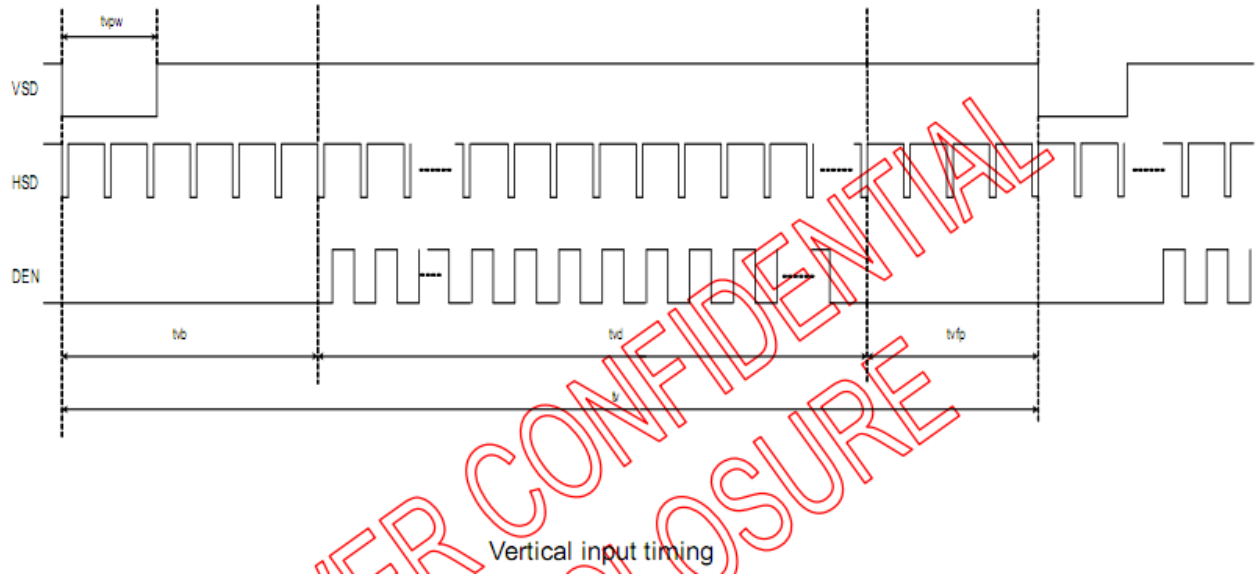
Item	Symbol	Values			Unit	Condition
		Min	Typ	Max		
Current for Driver	I_{GH}	-	0.5	-	mA	V_{GH} =18.0V
	I_{GL}	-	3.5	-	mA	V_{GL}=-6.0V
	IDV_{DD}	-	10.8	-	mA	DV_{DD} =1.8V
	IAV_{DD}	-	21.5	-	mA	AV_{DD} =9.6V

3.3 Power on/off Sequence

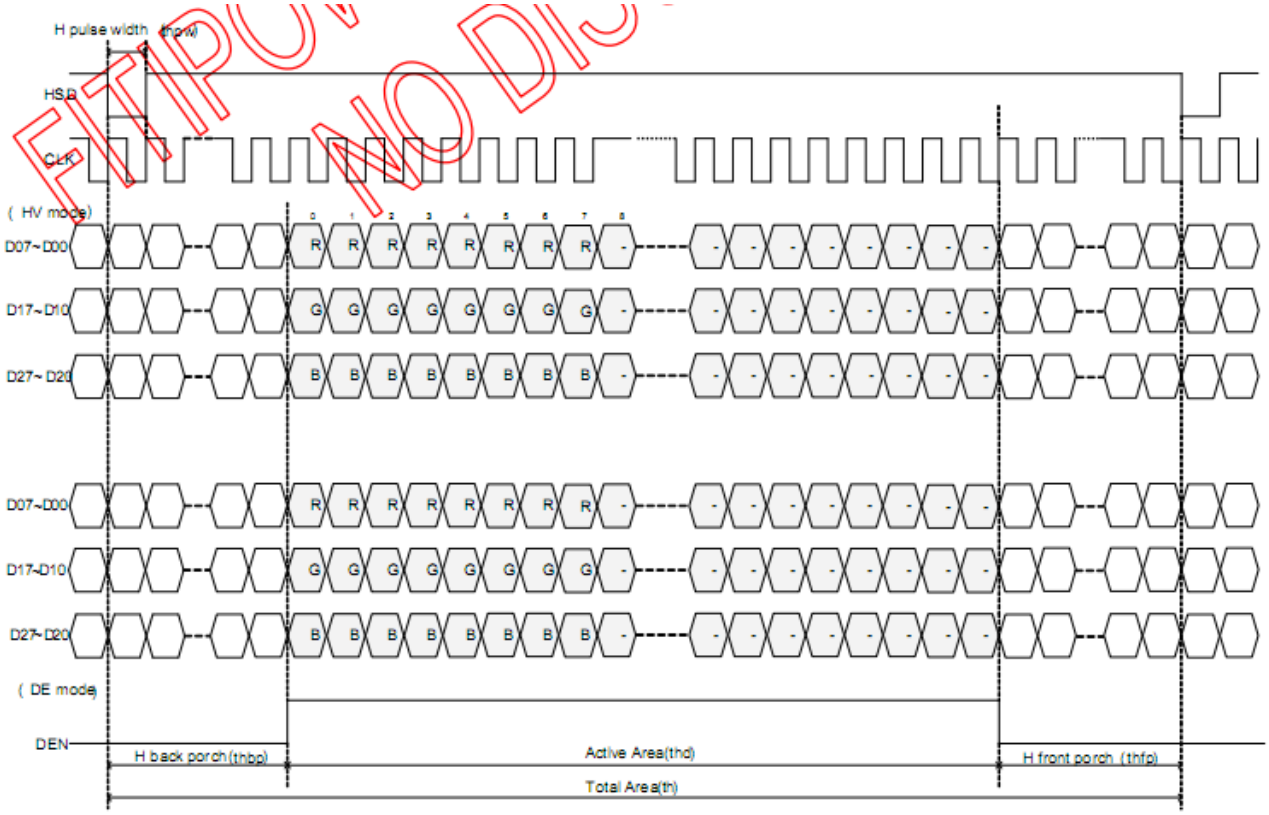


Note: CLK and Data Lanes should keep in LP11(stop state) before GRB.

3.4 Timing characteristics



Vertical input timing



Horizontal input timing

DE mode

Parameter	Symbol	Value			Unit
		Min.	Typ.	Max.	
DCLK frequency @Frame rate=60hz	fclk	40.8	51.2	67.2	Mhz
Horizontal display area	thd		1024		DCLK
HSYNC period time	th	1114	1344	1400	DCLK
HSYNC blanking	thb+thfp	90	320	376	DCLK
Vertical display area	Tvd		600		H
VSYNC period time	Tv	610	635	800	H
VSYNC blanking	Tvb+Tvfp	10	35	200	H

HV mode
Horizontal input timing

Parameter		Symbol	Value			Unit
Horizontal display area		thd	1024			DCLK
DCLK frequency@ Frame rate=60hz		fclk	Min.	Typ.	Max.	
			44.9	51.2	63	Mhz
1 Horizontal Line		th	1200	1344	1400	DCLK
HSYNC pulse width	Min.	thpw	1			
	Typ.		70			
	Max.		140			
HSYNC blanking		thb	160	160	160	
HSYNC front porch		thfp	16	160	216	

HV mode
Vertical input timing

Parameter	Symbol	Value			Unit
		Min.	Typ.	Max.	
Vertical display area	tvd		600		H
VSYNC period time	tv	624	635	750	H
VSYNC pulse width	tvpw	1	10	20	H
VSYNC back porch	tvb	23	23	23	H
VSYNC front porch	tvfp	1	12	127	H

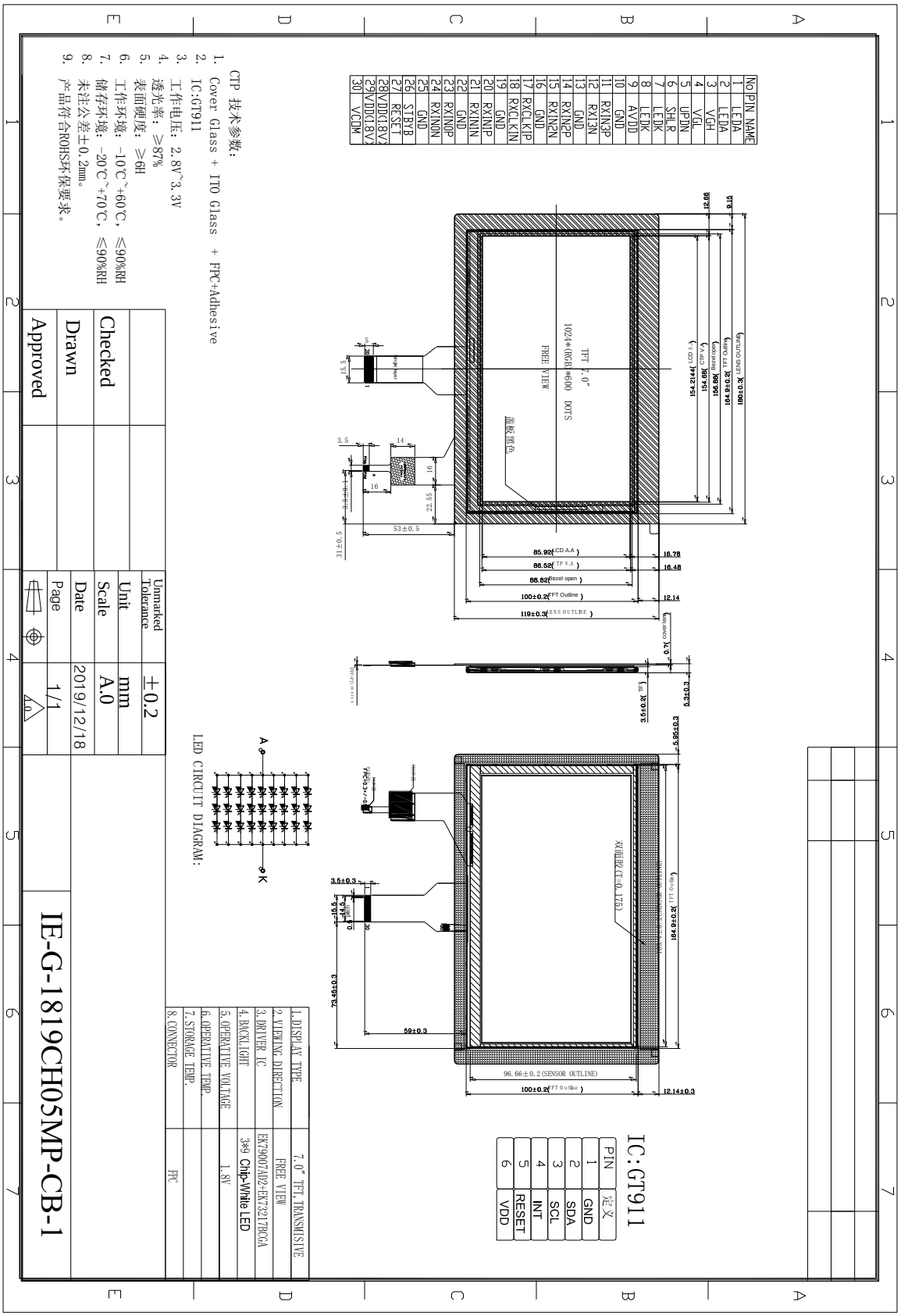
4. BACKLIGHT CHARACTERISTICS

Item	Symbol	Min	Typ	Max	Unit	Condition
Forward Voltage	Vf	--	9.6	--	V	If=180mA
Forward Current	If	--	180	-	mA	--
Operating Life Time	--	--	30000	--	Hrs	

Note 1: The LED Supply Voltage is defined by the number of LED at Ta=25℃

Note 2: Operating life means brightness goes down to 50% initial brightness. Typical operating life time is estimated data..

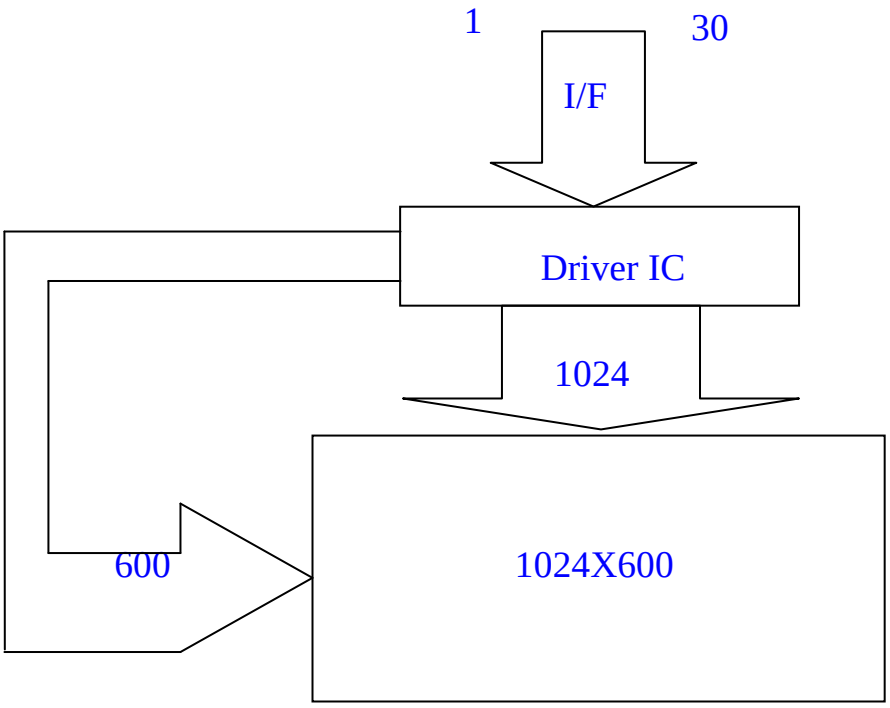
5. DIMENSIONAL DRAWING



6. INTERFACE PIN CONNECTIONS

Pin No.	Symbol	I/O	Function
1	LEDA	-	Power for LED backlight (Anode)
2	LEDA	-	Power for LED backlight (Anode)
3	VGH	-	Power for TFT -
4	VGL	-	Negative Power for TFT
5	UPDN	P	Up / Down Display Control
6	SHLR	I	Left or Right Display Control
7	LEDK	P	Power for LED backlight (Cathode)
8	LEDK	I	Power for LED backlight (Cathode)
9	AVDD	I	Power for Analog Circuit
10	GND	I	Power ground
11	RXIN3P	I	MIPI-DSI Data differentialsignal input pins. (Data lane 3+)
12	RXIN3N	I	MIPI-DSI Data differentialsignal input pins. (Data lane 3-)
13	GND	I	Power ground
14	RXIN2P	I	MIPI-DSI Data differentialsignal input pins. (Data lane 2+)
15	RXIN2N	I	MIPI-DSI Data differentialsignal input pins. (Data lane 2-)
16	GND	I	Power ground
17	RXCLKIP	I	MIPI-DSI CLOCK differential signal input pins.
18	RXCLKIN	I	MIPI-DSI CLOCK differential signal input pins.
19	GND	I	Power ground
20	RXIN1P	I	MIPI-DSI Data differentialsignal inputpins. (Data lane 1+)
21	RXIN1N	I	MIPI-DSI Data differentialsignal input pins. (Data lane 1-)
22	GND	I	Power ground
23	RXIN0P	I	MIPI-DSI Data differentialsignal input pins. (Data lane 0+)
24	RXIN0N	I	MIPI-DSI Data differentialsignal input.pins. (Data lane 0+)
25	GND	I	Power ground
26	STBYB	I	Standby mode
27	RESET	I	Global reset pin.
28	VDD(1.8V)	I	Power supply(1.8V)
29	VDD(1.8V)	I	Power supply(1.8V)
30	VCOM	I	Common Voltage

7. BLOCK DIAGRAM OF LCM



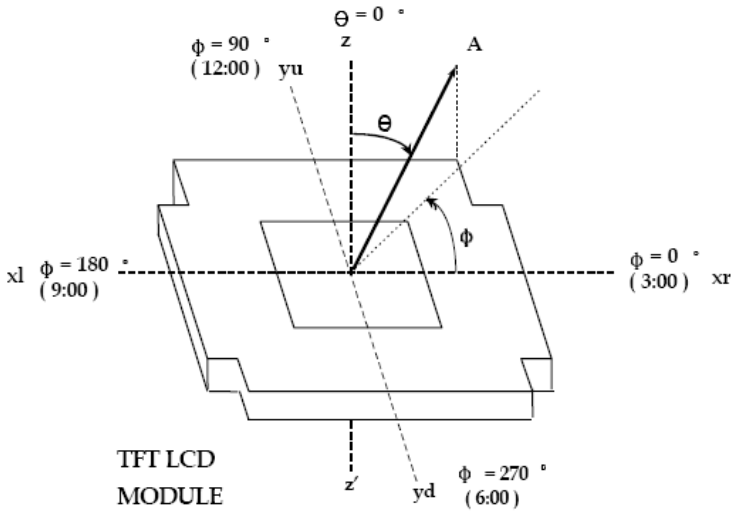
8. ELECTRO-OPTICAL CHARACTERISTICS

Optical characteristics are determined after the unit has been on and stable for approximately 30 minutes dark environment at 25℃.the value specified are at an approximate distance 500mm from the LCD surface at a viewing angle and θ equal to 0

8.1 LCD Optical Characteristics

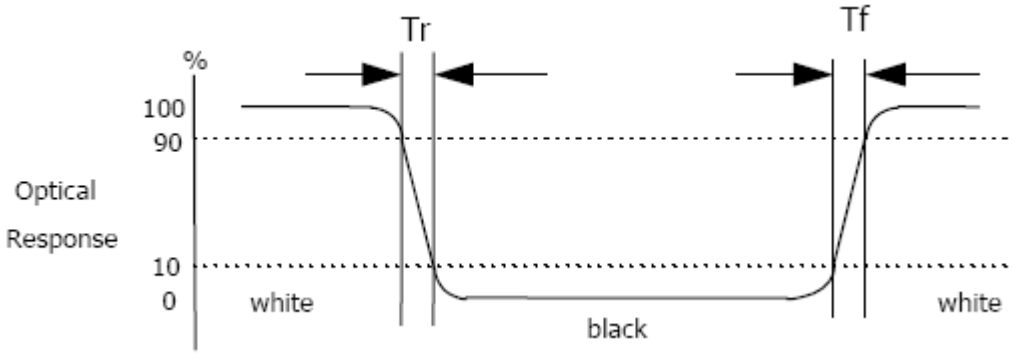
Parameter		Symbol	Condition	Min.	Typ.	Max.	Unit	Remark
Viewing Angle	Φ=3 O'clock	θ	Cr > 10	-	85	-	deg	Note 1
	Φ=9 O'clock			-	85	-		
	Φ= 6 O'clock			-	85	-		
	Φ=12 O'clock			-	85	-		
Response time		Tr+Tf	θ=0 Φ=0	-	30	-	ms	Note 2
Contrast ratio		Cr		-	800	-	-	Note 3
CIE(x,y) chromaticity	RED	X	θ=0 Φ=0	-	-	-	-	Note 4
		Y		-	-	-		
	GREEN	X		-	-	-		
		Y		-	-	-		
	BLUE	X		-	-	-		
		Y		-	-	-		
	WHITE	X		-	-	-		
		Y		-	-	-		
Uniformity(%)				80	-	-	-	Note 5
Luminance		L		-	400	-	-	Note 6

Note 1. LCD Viewing Angle



Viewing angle is the angle at which the contrast ratio is greater than 10.the angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface.

Note 2. Response time



Response time is the time required for the display to transition from white to black (Rising time, Tr) and from black to white (Falling time, Tf).for additional information

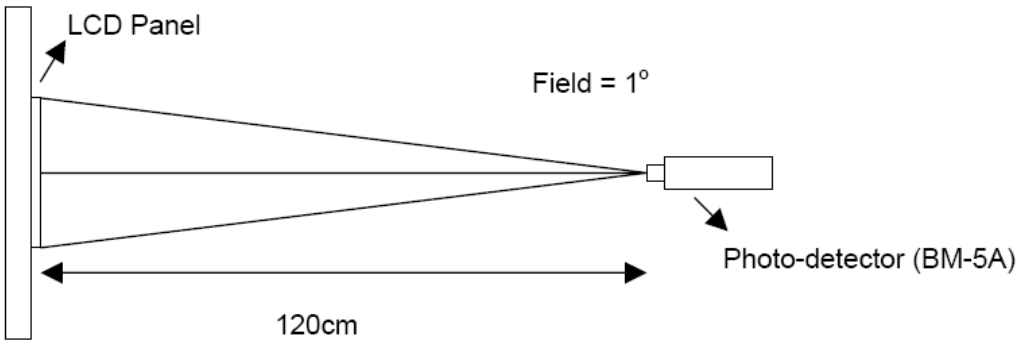
Note 3. Contrast Ratio(CR)

Contrast Ratio(CR) is defined mathematically as:

$$\text{Contrast Ratio} = \frac{\text{Surface Luminance with all white pixels}}{\text{Surface Luminance with all black pixels}}$$

Surface luminance is the center point across the lcd surface 500mm from the surface with all pixels displaying white.

Note 4. Definition of optical measurement setup

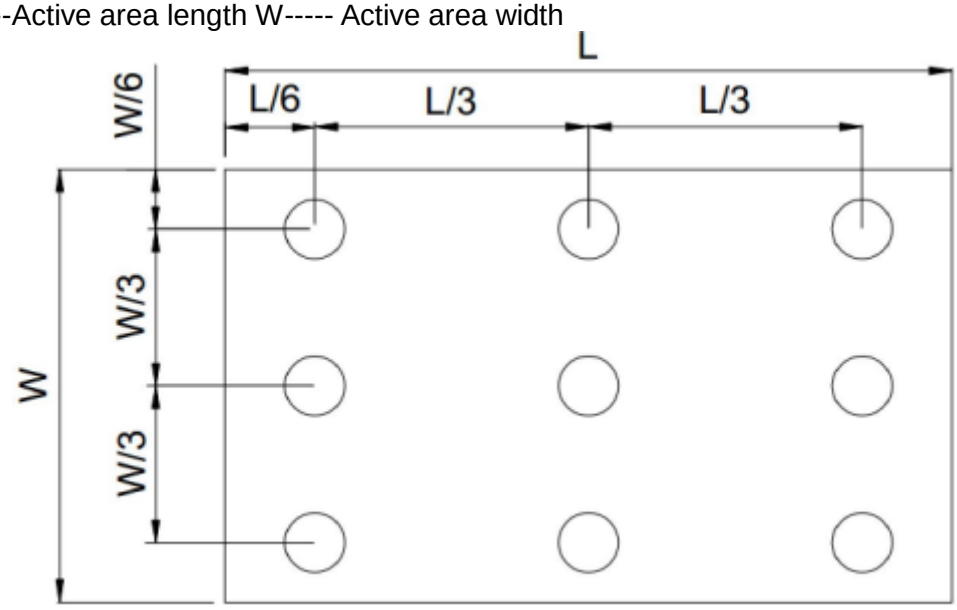


Note 5. Definition of luminance uniformity

Active area is divided into 9 measuring areas . Every measuring point is placed at the center of each measuring area.

Luminance Uniformity(U) = Lmin/ Lmax

L-------Active area length W----- Active area width



Lmax: The measured maximum luminance of all measurement position.

Lmin: The measured minimum luminance of all measurement position.

Note 6. Definition of luminance:

Measure the luminance of white state at center point.

9.0 TOUCH PANEL SPECIFICATION

9.1 General Specification

Item	Display Panel	Remark
Size	7.0 inch	/
Outline Dimension	180.0 x 119.0 x 1.7	mm
View Area	154.68 x 86.52	mm
Sensor Area	163.4 x 96.66	mm
Touch Panel Structure	G+G	/
Surface Hardness	≧6H	/
Transmittance	≧87%	/
Controller IC	GT911	/
Interface Type	I2C	/
Operating temperature &Humidity	-10℃～+60℃: 45%～90%RH	/
Storage temperature &Humidity	-20℃～+70℃: 45%～90%RH	/

9.2 FPC PIN Assignment

Pin.No	Symbol	Function
1	GND	Ground
2	SDA	IIC_SDA
3	SCL	IIC_SCL
4	INT	IIC_INT
5	RESET	IIC_RESET
6	VDD	Power supply3.3v

10.0 INSPECTION CRITERIA

10.1 Inspection Conditions

10.1.1 Environmental conditions

The environmental conditions for inspection shall be as follows

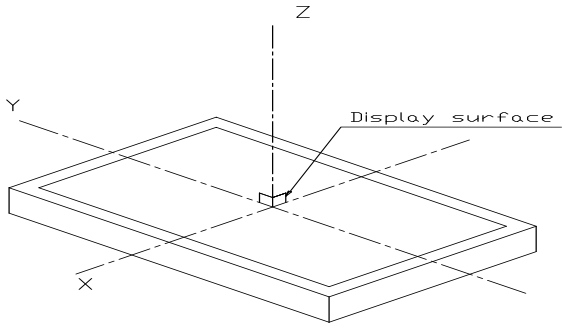
Room temperature: 20±3°C

Humidity: 65±20%RH

10.1.2 Environmental conditions

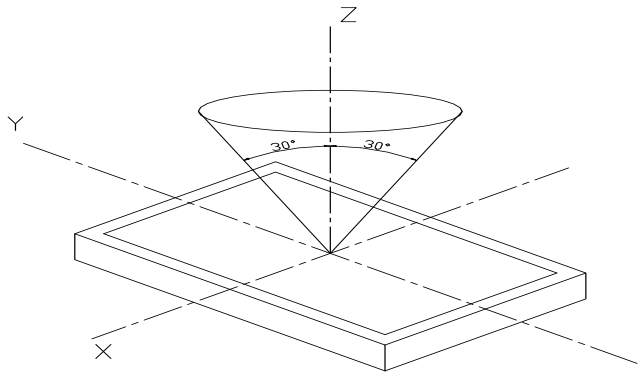
With a single 20-watt fluorescent lamp as the light source, the inspection was in the distance of 30cm or more from the LCD to the inspector's eyes.

10.2 Light Method



Fluorescent lamp perpendicular to the display surface

Inspection distance and angle



Inspection should be performed within angle φ (φ is usually 30°) from Z axis to each X and Y.

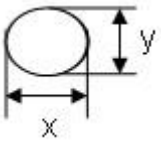
Inspection distance in any direction within φ must be kept 30±5cm from the display surface.

10.3 Classification of defects

10.3.1 Major defect

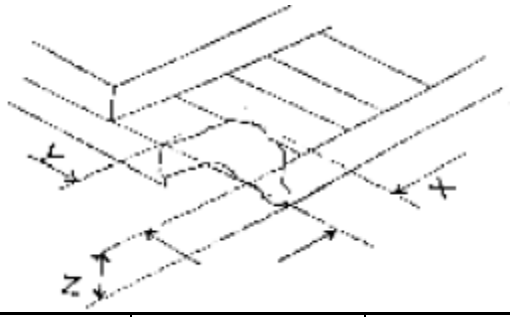
No.	Item	Inspection Standard	Classification of defects
1	All functional defects	1) No display 2) Display abnormally 3) Open or missing segment 4) Short circuit 5) Excess power consumption 6) Backlight no lighting, flickering and abnormal lighting	Major
2	Missing	Missing component	Major
3	Outline dimension	Overall outline dimension beyond the drawing is not allowed	Major

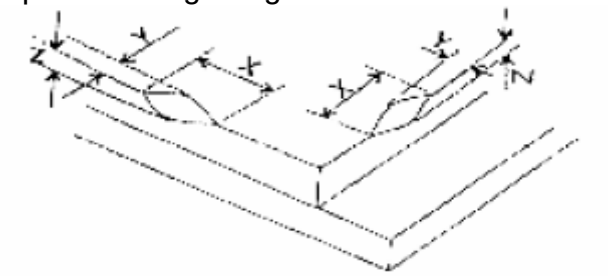
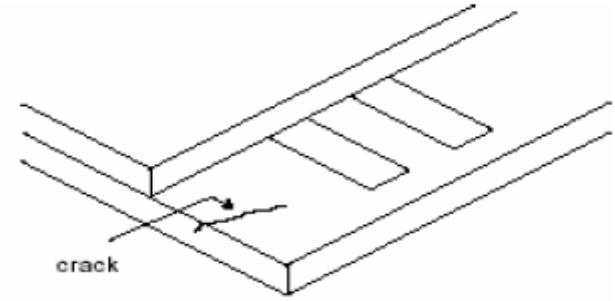
10.3.2 Cosmetic Defect

<table>
<tr><th>No.</th><th>Item</th><th colspan="2">Inspection Standard</th><th>Classification of defects</th></tr>
<tr><td rowspan="2">1</td><td rowspan="2">(spot defect)
Black and White spot pinhole</td><td>For dark/white spot, size Φ is defined as Φ=(x+y)/2</td><td>  </td><td rowspan="2">Minor</td></tr>
<tr><td colspan="2"><table><tr><th>Size Φ (mm)</th><th>Acceptable Quantity</th></tr><tr><td>Φ≤0.10</td><td>Ignore</td></tr><tr><td>0.10<Φ≤0.20</td><td>2</td></tr><tr><td>0.20<Φ≤0.30</td><td>2</td></tr><tr><td>0.30<Φ</td><td>0</td></tr></table></td></tr>
<tr><td>2</td><td>(line defect)
Black and White line</td><td colspan="2">Define:  </td><td>Minor</td></tr>
</table>

<table>
<tr><td></td><td>Polarizer scratch</td><td><table><tr><th>Width(mm)</th><th>Length(mm);Acceptable Qty</th></tr><tr><td>Φ≤0.03 L≤1.0</td><td>Ignore</td></tr><tr><td>0.03<W≤0.05</td><td>L≤3.0; N≤2</td></tr><tr><td>0.05<W or L>3.0</td><td>Define as spot defect</td></tr></table></td><td></td></tr>
<tr><td>3</td><td>Polarizer defect</td><td>Dent or bubble(between the polarizer and glass)<table><tr><th>Size Φ(mm)</th><th>Acceptable Qty</th></tr><tr><td>Φ≤0.10</td><td>Ignore</td></tr><tr><td>0.10<Φ≤0.2</td><td>2</td></tr><tr><td>0.20<Φ≤0.30</td><td>1</td></tr><tr><td>0.30<Φ</td><td>0</td></tr></table></td><td>Minor</td></tr>
</table>

10.3.3 Cosmetic Defect

<table>
<tr><th>No.</th><th>Item</th><th>Inspection Standard</th><th>Classification of defects</th></tr>
<tr><td>1</td><td>Glass defect</td><td>1) Chip on the corner
  <table><tr><th>X</th><th>Y</th><th>Z</th></tr><tr><td>≤3.0</td><td>≤S</td><td>≤T</td></tr></table>Remark: S=contact pad length;
T=the thickness of glass
Chips on the corner of terminal shall not be allowed to extend into the ITO pad or expose perimeter seal. Acceptable Quantity N≤2.</td><td>Minor</td></tr>
</table>

<table>
<tr>
<td></td>
<td></td>
<td>2) Chip on the edge of glass
 
<table>
<tr><td>X</td><td>Y</td><td>Z</td></tr>
<tr><td>Ignore</td><td>≤0.5</td><td>≤T</td></tr>
</table>
Acceptable Quantity: N≤2</td>
<td>Minor</td>
</tr>
<tr>
<td></td>
<td></td>
<td>3) Creak
Creaks tend to break are not allowed.
 
</td>
<td>Minor</td>
</tr>
</table>

11.0 LIABILITY AND INSPECTION STANDARD

NO.	Test Item	Test condition	Criterion
1	High Temperature Storage	70℃±2℃ 96H Restore 2H at 25℃ Power off	After testing, cosmetic and electrical defects should not happen.
2	Low Temperature Storage	-20℃±2℃ 96H Restore 2H at 25℃ Power off	
3	High Temperature Operation	60℃±2℃ 96H Restore 2H at 25℃ Power on	
4	Low Temperature Operation	-10℃±2℃ 96H Restore 2H at 25℃ Power on	
5	High Temperature & Humidity Storage	60℃±2℃ 90%RH 96H Power off	
6	Temperature Cycle	--20℃←→25℃←→70℃ 30min 5min 30min after 10cycle, Restore 2H at 25℃ Power off	
7	Vibration Test	10Hz~150Hz, 100m/s2, 120min	
8	Shock Test	Half-sinewave,300m/s2,11ms	
9	Drop Test(package state)	600mm, concrete floor,1corner, 3edges, 6 sides each time	1.After testing, cosmetic and electrical defects should not happen. 2.The product should remain at initial place. 3.Product uncovered or package broken is not permitted.

12 .0 PRECAUTIONS FOR USING LCD MODULE

12.1 Handing Precautions

(1) The display panel is made of glass and polarizer. As glass is fragile, it tends to become or chipped during handling especially on the edges. Please avoid dropping or jarring. Do not subject it to a mechanical shock by dropping it or impact.

(2) If the display panel is damaged and the liquid crystal substance leaks out, be sure not to get any in your mouth. If the substance contacts your skin or clothes, wash it off using soap and water.

(3) Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary. Do not touch the display with bare hands. This will stain the display area and degraded insulation between terminals (some cosmetics are determined to the polarizer).

(4) The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully. Do not touch, push or rub the exposed polarizers with anything harder than an HB pencil lead (glass, tweezers, etc.). Do not put or attach anything on the display area to avoid leaving marks on. Condensation on the surface and contact with terminals due to cold will damage, stain or dirty the polarizer. After products are tested at low temperature they must be warmed up in a container before coming is contacting with room temperature air.

(5) If the display surface becomes contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If it is heavily contaminated, moisten cloth with one of the following solvents
- Isopropyl alcohol
- Ethyl alcohol

Do not scrub hard to avoid damaging the display surface.

(6) Solvents other than those above-mentioned may damage the polarizer. Especially, do not use the following.
- Water
- Ketone
- Aromatic solvents

Wipe off saliva or water drops immediately, contact with water over a long period of time may cause deformation or color fading. Avoid contacting oil and fats.

(7) Exercise care to minimize corrosion of the electrode. Corrosion of the electrodes is accelerated by water droplets, moisture condensation or a current flow in a high-humidity environment.

(8) Install the LCD Module by using the mounting holes. When mounting the LCD module make

sure it is free of twisting, warping and distortion. In particular, do not forcibly pull or bend the I/O cable or the backlight cable.

(9) Do not attempt to disassemble or process the LCD module.

(10) NC terminal should be open. Do not connect anything.

(11) If the logic circuit power is off, do not apply the input signals.

(12) Since LCM has been assembled and adjusted with a high degree of precision, avoid applying excessive shocks to the module or making any alterations or modifications to it.

- Do not alter, modify or change the shape of the tab on the metal frame.
- Do not make extra holes on the printed circuit board, modify its shape or change the positions of components to be attached.
- Do not damage or modify the pattern writing on the printed circuit board.
- Absolutely do not modify the zebra rubber strip (conductive rubber) or heat seal connector.
- Except for soldering the interface, do not make any alterations or modifications with a soldering iron.
- Do not drop, bend or twist LCM.

12.2 Storage Precautions

When storing the LCD modules, the following precaution is necessary.

(1) Store them in a sealed polyethylene bag. If properly sealed, there is no need for the dessicant.

(2) Store them in a dark place. Do not expose to sunlight or fluorescent light, keep the temperature between 0°C and 35°C.

(3) The polarizer surface should not come in contact with any other objects. (We advise you to store them in the container in which they were shipped).

12.3 Others

Liquid crystals solidify under low temperature (below the storage temperature range) leading to defective orientation or the generation of air bubbles (black or white). Air bubbles may also be generated if the module is subject to a low temperature.

If the LCD modules have been operating for a long time showing the same display patterns, the display patterns may remain on the screen as ghost images and a slight contrast irregularity may also appear. A normal operating status can be regained by suspending use for some time. It should be noted that this phenomenon does not adversely affect performance reliability.