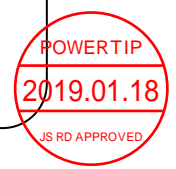


### SPECIFICATIONS

<b>CUSTOMER</b>	:	<b>PTC</b>
<b>SAMPLE CODE</b>	:	<b>SH240320T068-LAA06</b>
<b>MASS PRODUCTION CODE</b>	:	<b>PH240320T068-LAA06</b>
<b>SAMPLE VERSION</b>	:	<b>01</b>
<b>SPECIFICATIONS EDITION</b>	:	<b>001</b>
<b>DRAWING NO. (Ver.)</b>	:	<b>JLMD-PH240320T068-LAA06_001</b>
<b>PACKAGING NO. (Ver.)</b>	:	

**Customer Approved**

**Date:**



Approved	Checked	Designer
閔偉	劉進	陳璐

- Preliminary specification for design input
- Specification for sample approval

### POWER TIP TECH. CORP.

**Headquarters:**

No.8, 6 <sup>th</sup> Road, Taichung Industrial Park,	TEL: 886-4-2355-8168	E-mail: <a href="mailto:sales@powertip.com.tw">sales@powertip.com.tw</a>
Taichung, Taiwan	FAX: 886-4-2355-8166	Http://www.powertip.com.tw
台中市 407 工業區六路 8 號		



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- 1.2 Mechanical Specifications
- 1.3 Absolute Maximum Ratings
- 1.4 DC Electrical Characteristics
- 1.5 Optical Characteristics
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- 2.2 Interface Pin Description
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### 3. QUALITY ASSURANCE SYSTEM

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- 3.2 Inspection Specification

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- 5.2 Handling
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Appendix : LCM Drawing

Note : For detailed information please refer to IC data sheet :

Primacy(TFT LCD): Sitronix : ST7789VI

## 1. SPECIFICATIONS

### 1.1 Features

#### Main LCD panel

Item	Standard Value
Display Type	240(R、G、B) * 320 Dots
LCD Type	Normally white , Transmissive type
Screen size(inch)	2.8 inch
Viewing Direction	12 O'clock
Color configuration	RGB-Strip
Interface	80-16bit parallel I/F II
Other(controller/driver IC)	Sitronix: ST7789VI
ROHS	THIS PRODUCT CONFORMS THE ROHS OF PTC Detail information please refer website : <a href="http://www.powertip.com.tw/news.php?area_id_view=1085560481/">http://www.powertip.com.tw/news.php?area_id_view=1085560481/</a>

### 1.2 Mechanical Specifications

Item	Standard Value	Unit
Outline Dimension	50.0(W) * 69.2 (L) * 3.05 (H)max	mm

#### LCD panel

Item	Standard Value	Unit
Active Area	43.2 (W) * 57.6 (L)	mm

### 1.3 Absolute Maximum Ratings

#### Module

Item	Symbol	Condition	Min.	Max.	Unit
System Power Supply Voltage	VCC	-	-0.3	+4.6	V
	VGH ~ VGL	-	-0.3	+30	V
Input Voltage	VIN	-	-0.3	VCC+0.5	V
Operating Temperature	T <sub>OP</sub>	-	-20	+70	°C
Storage Temperature	T <sub>ST</sub>	-	-30	+80	°C
Storage Humidity	H <sub>D</sub>	T <sub>a</sub> ≅ 40 °C	20	90	%RH

### 1.4 DC Electrical Characteristics

#### Module

GND = 0V, T<sub>a</sub> = 25°C

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Power Supply Voltage1	VCC	-	2.4	2.8	3.6	V
Input High Voltage	V <sub>IH</sub>	-	0.7 VCC	-	VCC	V
Input Low Voltage	V <sub>IL</sub>	-	GND	-	0.3 VCC	V
Output High Voltage	V <sub>OH</sub>	I <sub>OH</sub> =-0.1mA	0.8*VCC	-	VCC	V
Output Low Voltage	V <sub>OL</sub>	I <sub>OL</sub> =0.1mA	GND	-	0.2*VCC	V
Supply Current	ICC	VCC = 2.8V	-	(9)	(12)	mA

Note1:Maximum current display

## 1.5 Optical Characteristics

### TFT LCD Module

VCC = 2.8V, Ta=25°C

Item	Symbol	Condition	Min.	Typ.	Max.	unit	-	
Response time	Tr+ Tf	-	-	(30)	(45)	ms	Note2	
Viewing angle	Top	$\theta Y+$	CR $\geq$ 10	-	60	-	Deg.	Note4
	Bottom	$\theta Y-$		-	60	-		
	Left	$\theta X-$		-	60	-		
	Right	$\theta X+$		-	60	-		
Contrast ratio	CR	-	500	600	-	-	Note3	
Color of CIE Coordinate ( With B/L )	White	X	IF=80 mA	(0.25)	(0.30)	(0.35)	-	Note1
		Y		(0.28)	(0.33)	(0.38)		
	Red	X		(0.58)	(0.63)	(0.68)		
		Y		(0.30)	(0.35)	(0.40)		
	Green	X		(0.29)	(0.34)	(0.39)		
		Y		(0.56)	(0.61)	(0.66)		
	Blue	X		(0.09)	(0.14)	(0.19)		
		Y		(0.02)	(0.07)	(0.12)		
Average Brightness Pattern=white display (With B/L) *1	IV	IF=80 mA	(230)	(255)	-	cd/m <sup>2</sup>		
Uniformity (With B/L)*2	$\Delta B$	IF=80 mA	70	-	-	%		

Note 1:

\*1 :  $\Delta B = B(\min) / B(\max) * 100\%$

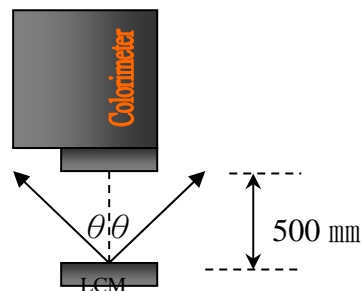
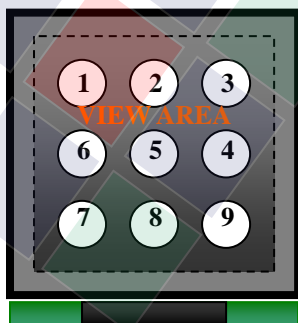
\*2 : Measurement Condition for Optical Characteristics:

a : Environment: 25°C±5°C / 60±20%R.H , no wind , dark room below 10 Lux at typical lamp current and typical operating frequency.

b : Measurement Distance: 500 ± 50 mm , ( $\theta = 0^\circ$ )

c : Equipment: TOPCON BM-7 fast , (field 1°) , after 10 minutes operation.

d : The uncertainty of the C.I.E coordinate measurement  $\pm 0.01$  , Average Brightness  $\pm 4\%$



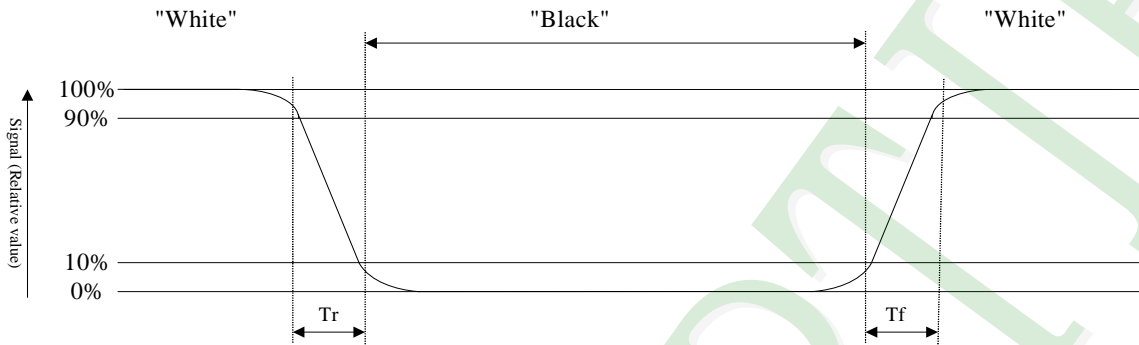
Colorimeter=BM-7 fast

To be measured at the center area of panel with a viewing cone of 1° by Topcon luminance meter BM-7, after 10 minutes operation (module)

**Note2: Definition of response time:**

The output signals of photo detector are measured when the input signals are changed from “black” to “white”(falling time) and from “white” to “black”(rising time), respectively. The response time is defined as the time interval between the 10% and 90% of Amplitudes.

Refer to figure as below:



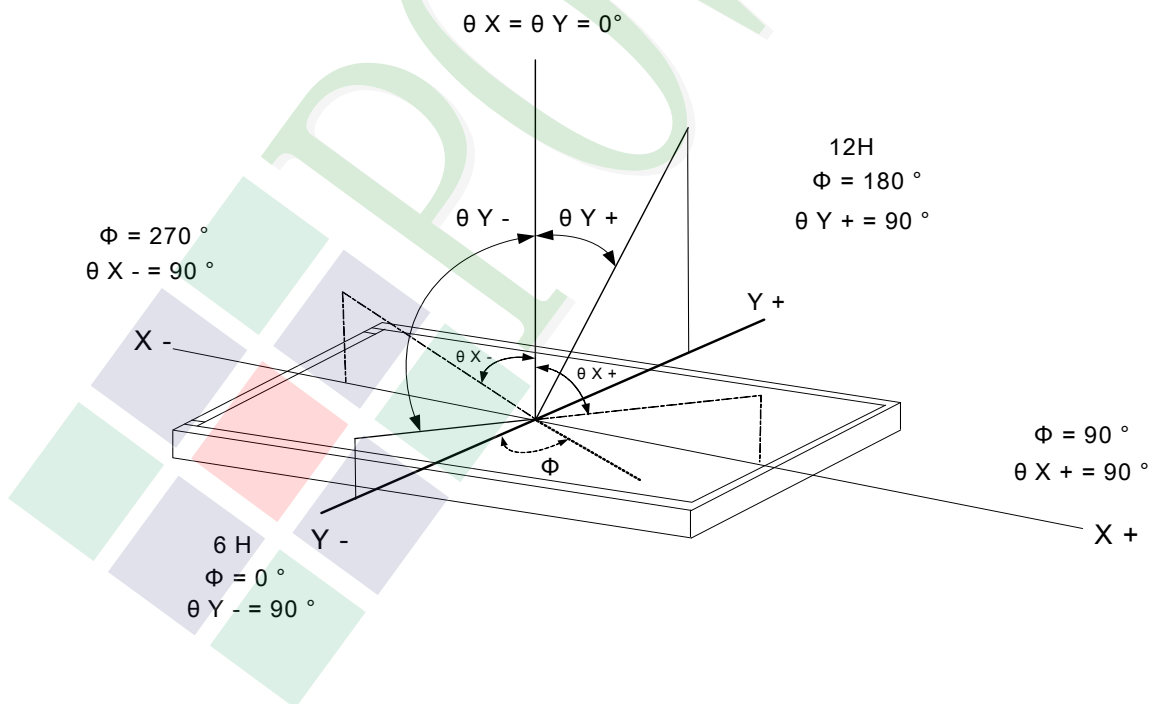
**Note3: Definition of contrast ratio:**

Contrast ratio is calculated with the following formula

$$\text{Contrast ratio (CR)} = \frac{\text{Photo detector output when LCD is at "White" state}}{\text{Photo detector output when LCD is at "Black" state}}$$

**Note4: Definition of viewing angle:**

Refer to figure as below:



## 1.6 Backlight Characteristics

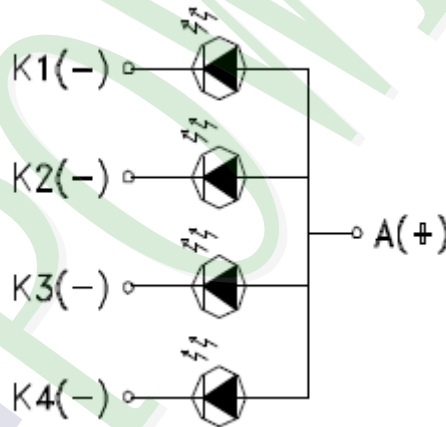
### Maximum Ratings

Item	Symbol	Conditions	Min.	Max.	Unit
Forward Current	IF	Ta =25°C	-	30*4	mA
Reverse Voltage	VR	Ta =25°C	-	5	V
Power Dissipation	PD	Ta =25°C	-	90*4	mW

### Electrical / Optical Characteristics

Item	Symbol	Conditions	Min.	Typ.	Max.	Unit
Forward Voltage	VF	IF= 80 mA	3.0	-	3.6	V
Average Brightness (without LCD)	IV		5000	5500	-	cd/m <sup>2</sup>
CIE Color Coordinate (Without LCD)	X		0.26	0.28	0.33	-
	Y		0.26	0.28	0.33	
Color			White			

### Internal Circuit



### Other Description

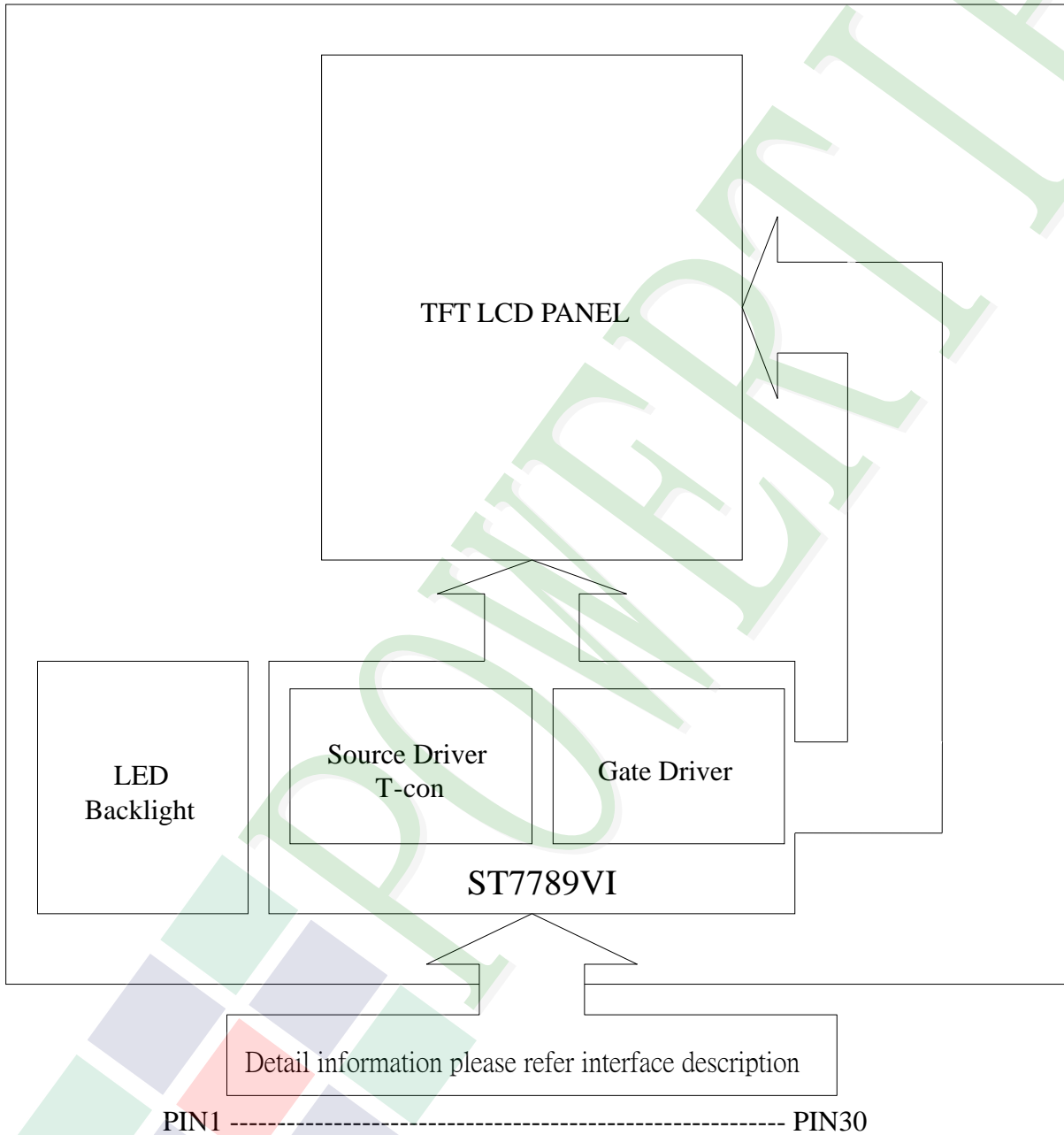
Item	Conditions	Description
Life Time	Ta =25°C IF= 80 mA	20000 hrs

## 2.1 Counter Drawing

### 2.1.1 LCM Mechanical Diagram

\* See Appendix

### 2.1.2 Block Diagram



## 2.2 Interface Pin Description

Pin No.	Symbol	Function
1	LEDK1-4	Power supply for LED Backlight Cathode input
2	LEDA	Power supply for LED Backlight Anode input
3	GND	Signal ground.(0V)
4	RESET	Reset input pin for TFT LCD. When RESET is "L", initialization is executed.
5	DB17	Bi-directional data bus
6	DB16	
7	DB15	
8	DB14	
9	DB13	
10	DB12	
11	DB11	
12	DB10	
13	DB8	
14	DB7	
15	DB6	
16	DB5	
17	DB4	
18	DB3	
19	DB2	
20	DB1	
21	RD	Read signal input , active at Low.
22	WR/SCL	Write signal input , active at Low.
23	RS	When RS = 0: Command.    When RS = 1: Display data.
24	CS	Chip select signal , Active at "L"
25	XR	NC

Pin No.	Symbol	Function
26	YD	NC
27	XL	
28	YU	
29	GND	Signal ground.(0V)
30	2.8 /VCC	Power supply for the internal logic circuit.

## 2.3 Timing Characteristics

### 8080 Series MCU Parallel Interface Characteristics: 18/16/9/8-bit Bus

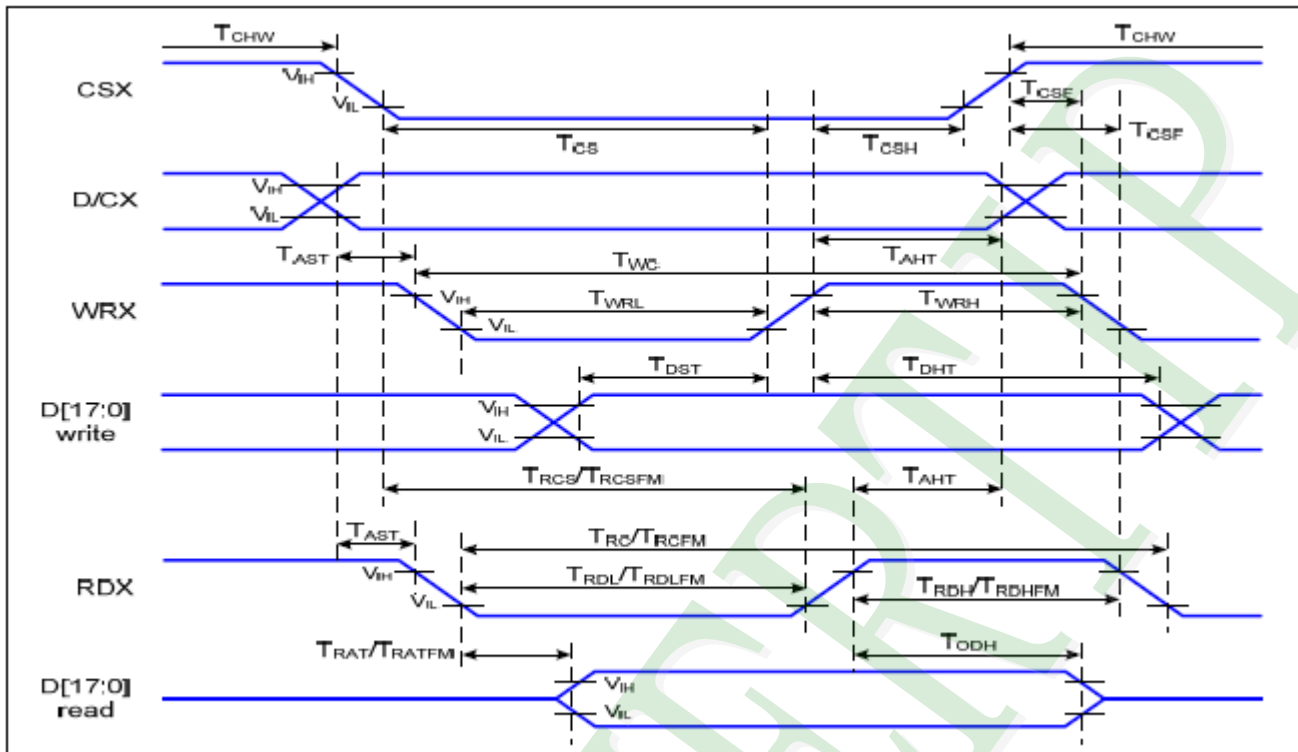
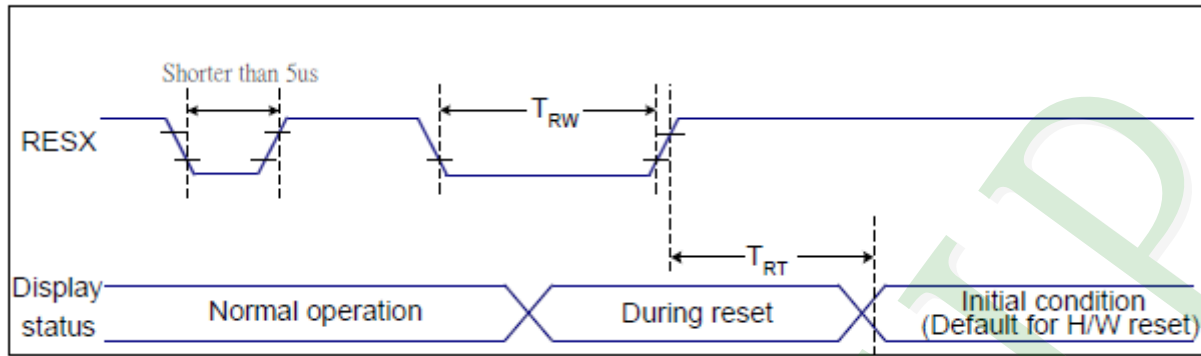


Figure 1 Parallel Interface Timing Characteristics (8080-Series MCU Interface)

Signal	Symbol	Parameter	Min	Max	Unit	Description
D/CX	$T_{AST}$	Address setup time	0		ns	
	$T_{AHT}$	Address hold time (Write/Read)	10		ns	
CSX	$T_{CHW}$	Chip select "H" pulse width	0		ns	
	$T_{CS}$	Chip select setup time (Write)	15		ns	
	$T_{RCS}$	Chip select setup time (Read ID)	45		ns	
	$T_{RCSFM}$	Chip select setup time (Read FM)	355		ns	
	$T_{CSF}$	Chip select wait time (Write/Read)	10		ns	
	$T_{CSH}$	Chip select hold time	10		ns	
WRX	$T_{WC}$	Write cycle	66		ns	
	$T_{WRH}$	Control pulse "H" duration	15		ns	
	$T_{WRL}$	Control pulse "L" duration	15		ns	
RDX (ID)	$T_{RC}$	Read cycle (ID)	160		ns	When read ID data
	$T_{RDH}$	Control pulse "H" duration (ID)	90		ns	
	$T_{RDL}$	Control pulse "L" duration (ID)	45		ns	
RDX (FM)	$T_{RCFM}$	Read cycle (FM)	450		ns	When read from frame memory
	$T_{RDHFM}$	Control pulse "H" duration (FM)	90		ns	
	$T_{RDLFM}$	Control pulse "L" duration (FM)	355		ns	
D[17:0]	$T_{DST}$	Data setup time	10		ns	For CL=30pF
	$T_{DHT}$	Data hold time	10		ns	
	$T_{RAT}$	Read access time (ID)		40	ns	
	$T_{RATFM}$	Read access time (FM)		340	ns	
	$T_{ODH}$	Output disable time	20	80	ns	

**8080 Parallel Interface Characteristics**

## Reset Timing:



Reset Timing

VDDI=1.65 to 3.6V, VDD=2.4 to 3.6V, AGND=DGND=0V, Ta=25 °C

Related Pins	Symbol	Parameter	MIN	MAX	Unit
RESX	TRW	Reset pulse duration	10	-	us
	TRT	Reset cancel	-	5 (Note 1, 5)	ms
				120 (Note 1, 6, 7)	ms

## Reset Timing

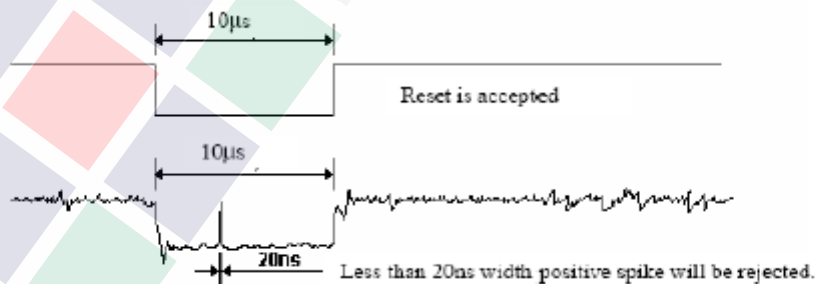
### Notes:

- The reset cancel includes also required time for loading ID bytes, VCOM setting and other settings from NVM (or similar device) to registers. This loading is done every time when there is HW reset cancel time (tRT) within 5 ms after a rising edge of RESX.
- Spike due to an electrostatic discharge on RESX line does not cause irregular system reset according to the table below:

RESX Pulse	Action
Shorter than 5us	Reset Rejected
Longer than 9us	Reset
Between 5us and 9us	Reset starts

- During the Resetting period, the display will be blanked (The display is entering blanking sequence, which maximum time is 120 ms, when Reset Starts in Sleep Out -mode. The display remains the blank state in Sleep In -mode.) and then return to Default condition for Hardware Reset.

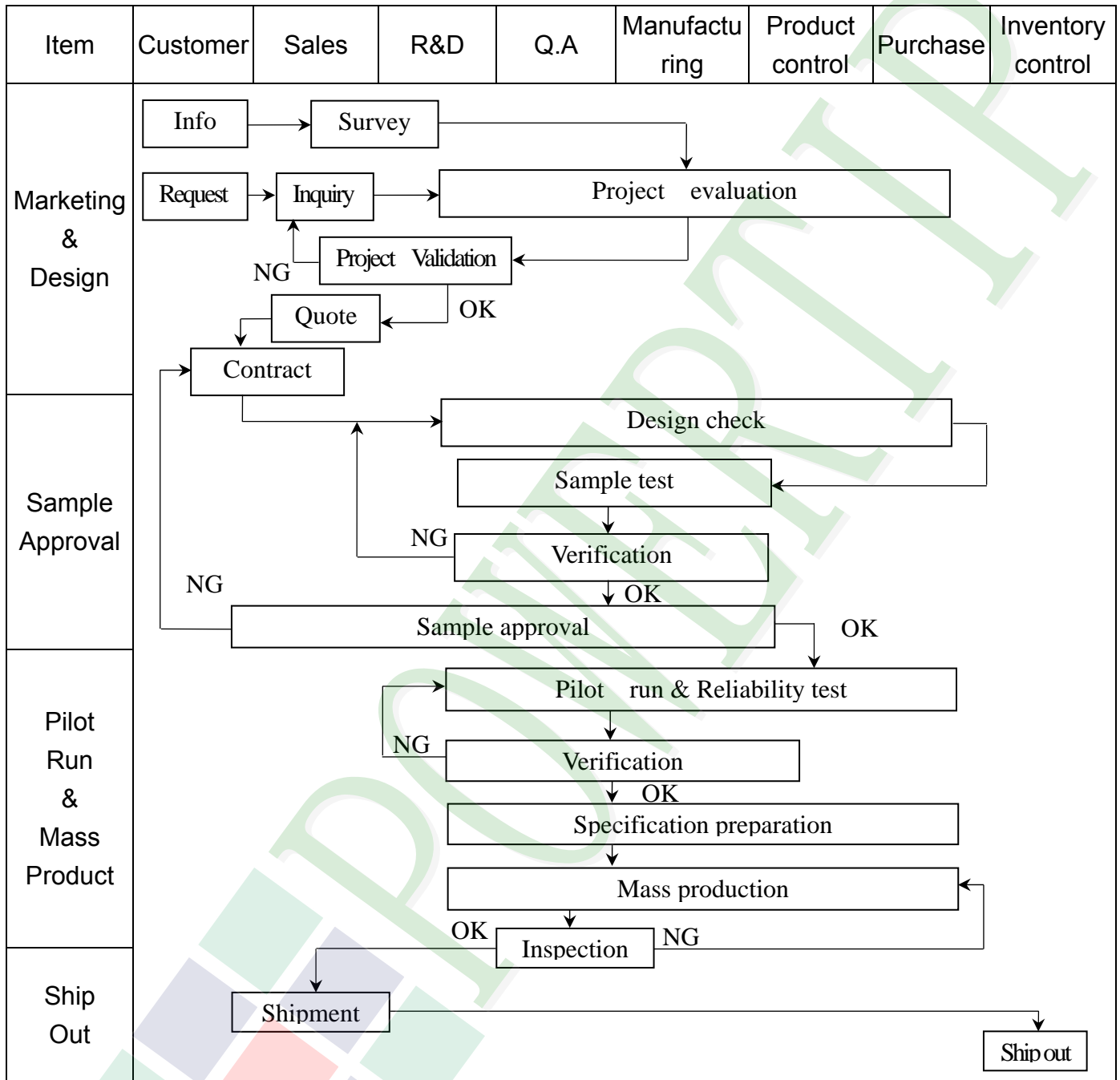
- Spike Rejection also applies during a valid reset pulse as shown below:

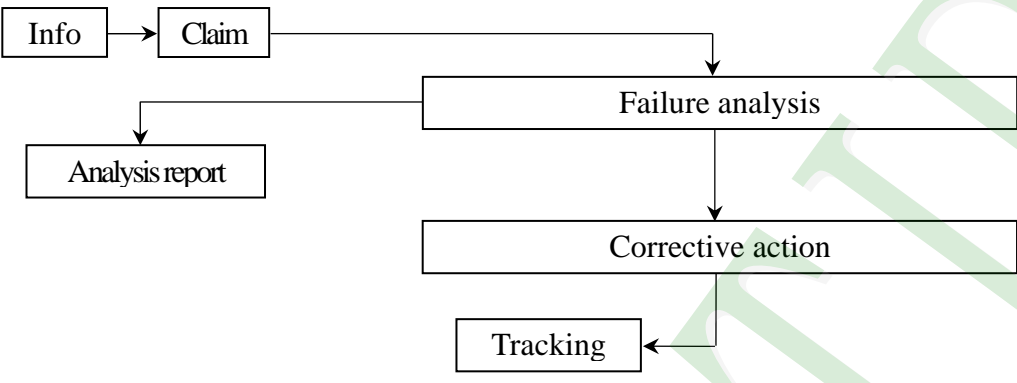


- When Reset applied during Sleep In Mode.
- When Reset applied during Sleep Out Mode.
- It is necessary to wait 5msec after releasing RESX before sending commands. Also Sleep Out command cannot be sent for 120msec.

### 3. QUALITY ASSURANCE SYSTEM

#### 3.1 Quality Assurance Flow Chart



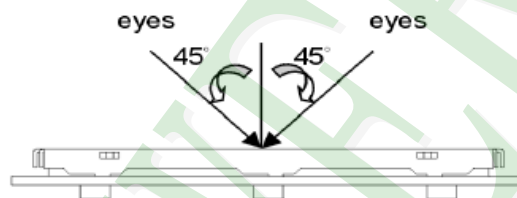
Item	Customer	Sales	R&D	Q.A	Manufacturing	Product control	Purchase	Inventory control
Sales Service	 <pre> graph TD     Info[Info] --&gt; Claim[Claim]     Claim --&gt; FA[Failure analysis]     Claim --&gt; AR[Analysis report]     FA --&gt; CA[Corrective action]     CA --&gt; Tracking[Tracking]           </pre>							
Q.A Activity	1. ISO 9001 Maintenance Activities 3. Equipment calibration 5. Standardization Management				2. Process improvement proposal 4. Education And Training Activities			

## 3.2 Inspection Specification

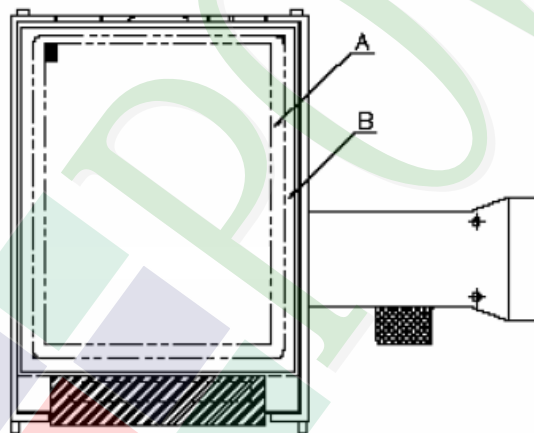
- ◆ Scope : The document shall be applied to TFT-LCD Module for less than 3.5" (Ver.B01).
- ◆ Inspection Standard : MIL-STD-105E Table Normal Inspection Single Sampling Level II .
- ◆ Equipment : Gauge 、 MIL-STD 、 Powertip Tester 、 Sample
- ◆ Defect Level : Major Defect AQL : 0.4 ; Minor Defect AQL : 1.5
- ◆ OUT Going Defect Level : Sampling.
- ◆ Standard of the product appearance test :

a. Manner of appearance test :

- (1). The test best be under 20W×2 fluorescent light , and distance of view must be at 30 cm.
- (2). The test direction is base on about around 45° of vertical line.



(3). Definition of area.



*A* area : viewing area

*B* area : Outside of viewing area

(4). Standard of inspection : (Unit : mm)

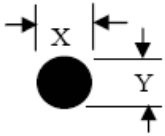
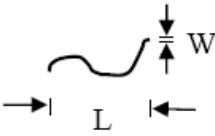
**◆ Specification For TFT-LCD Module Less Than 3.5" :**

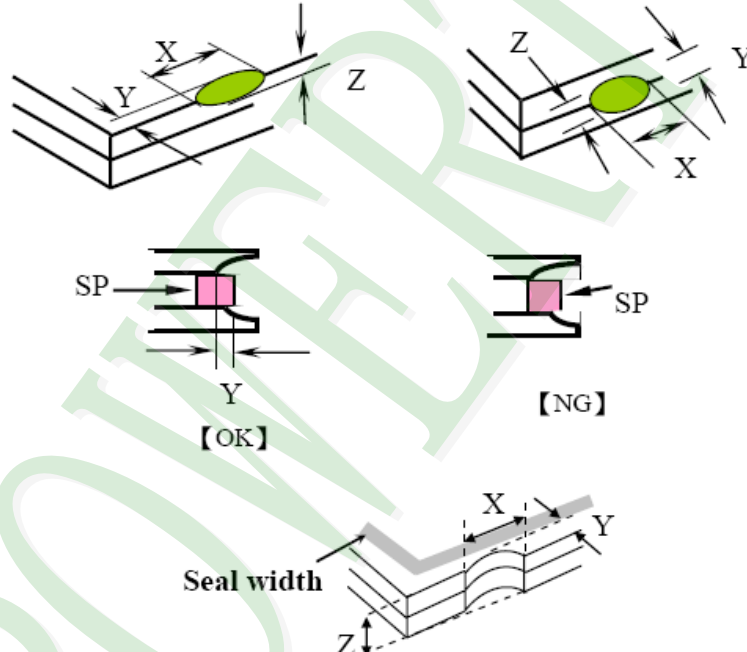
(Ver.B01)

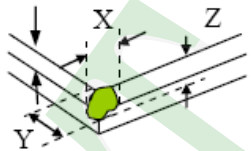
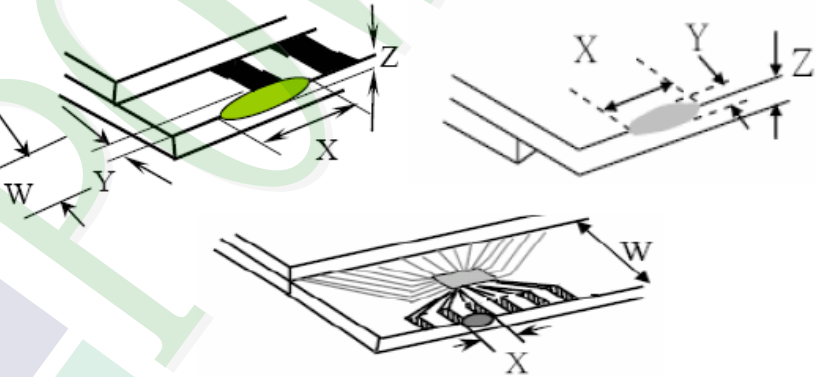
NO	Item	Criterion	Level										
01	Product condition	1. 1 The part number is inconsistent with work order of production.	Major										
		1. 2 Mixed product types.	Major										
		1. 3 Assembled in inverse direction.	Major										
02	Quantity	2. 1 The quantity is inconsistent with work order of production.	Major										
03	Outline dimension	3. 1 Product dimension and structure must conform to structure diagram.	Major										
04	Electrical Testing	4. 1 Missing line character and icon.	Major										
		4. 2 No function or no display.	Major										
		4. 3 Display malfunction.	Major										
		4. 4 LCD viewing angle defect.	Major										
		4. 5 Current consumption exceeds product specifications.	Major										
05	<b>Dot defect</b> (Bright dot , Dark dot)  On -display	<table border="1" style="margin-left: auto; margin-right: auto;"> <thead> <tr> <th>Item</th> <th>Acceptance (Q'ty)</th> </tr> </thead> <tbody> <tr> <td>Bright Dot</td> <td><math>\leq 2</math></td> </tr> <tr> <td>Dark Dot</td> <td><math>\leq 3</math></td> </tr> <tr> <td>Joint Dot</td> <td><math>\leq 2</math></td> </tr> <tr> <td>Total</td> <td><math>\leq 3</math></td> </tr> </tbody> </table>	Item	Acceptance (Q'ty)	Bright Dot	$\leq 2$	Dark Dot	$\leq 3$	Joint Dot	$\leq 2$	Total	$\leq 3$	Minor
		Item	Acceptance (Q'ty)										
		Bright Dot	$\leq 2$										
		Dark Dot	$\leq 3$										
		Joint Dot	$\leq 2$										
Total	$\leq 3$												
5. 1 Inspection pattern : full white , full black , Red , Green and blue screens.													
5. 2 It is defined as dot defect if defect area $> 1/2$ dot.													
5. 3 The distance between two dot defect $\geq 5$ mm.													

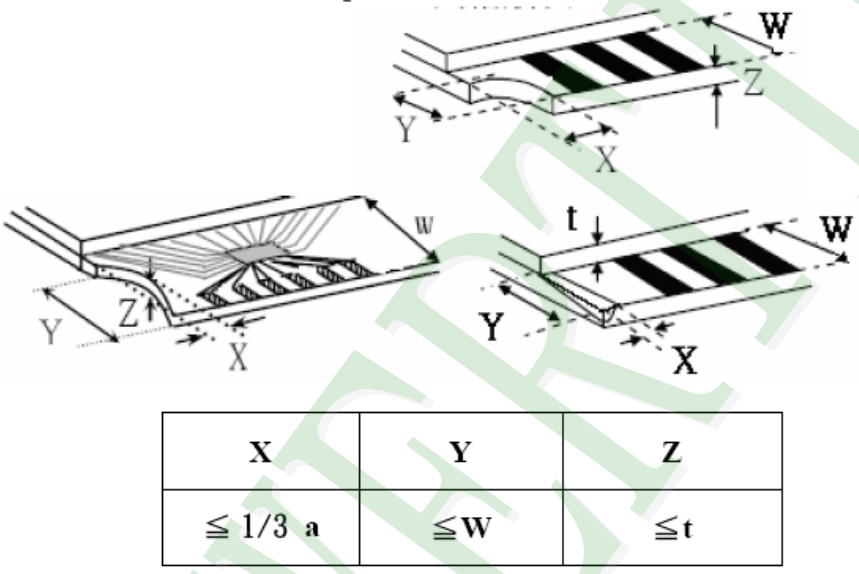
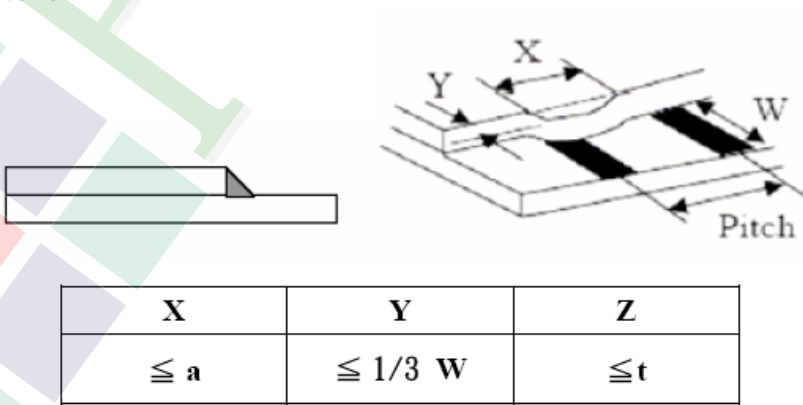
**◆ Specification For TFT-LCD Module Less Than 3.5" :**

(Ver.B01)

NO	Item	Criterion	Level																																								
06	<p>Black or white dot、scratch、contamination</p> <p>Round type</p>  <p><math>\Phi = (x + y) / 2</math></p> <p>Line type</p> 	<p>6.1 Round type ( Non-display or display ) :</p> <table border="1" data-bbox="552 416 1321 869"> <thead> <tr> <th rowspan="2">Dimension (diameter : <math>\Phi</math>)</th> <th colspan="2">Acceptance (Q'ty)</th> </tr> <tr> <th>A area</th> <th>B area</th> </tr> </thead> <tbody> <tr> <td><math>\Phi \leq 0.15</math></td> <td colspan="2">Ignore</td> </tr> <tr> <td><math>0.15 &lt; \Phi \leq 0.20</math></td> <td>2</td> <td rowspan="3">Ignore</td> </tr> <tr> <td><math>0.20 &lt; \Phi \leq 0.30</math></td> <td>2</td> </tr> <tr> <td><math>\Phi &gt; 0.30</math></td> <td>0</td> </tr> <tr> <td><b>Total</b></td> <td colspan="2">3</td> </tr> </tbody> </table> <p>6.2 Line type( Non-display or display ) :</p> <table border="1" data-bbox="533 983 1342 1397"> <thead> <tr> <th colspan="2">Dimension</th> <th colspan="2">Acceptance (Q'ty)</th> </tr> <tr> <th>Length (L)</th> <th>Width (W)</th> <th>A area</th> <th>B area</th> </tr> </thead> <tbody> <tr> <td>---</td> <td><math>W \leq 0.03</math></td> <td>Ignore</td> <td rowspan="3">Ignore</td> </tr> <tr> <td><math>L \leq 5.0</math></td> <td><math>0.03 &lt; W \leq 0.05</math></td> <td>3</td> </tr> <tr> <td>---</td> <td><math>W &gt; 0.05</math></td> <td>As round type</td> </tr> <tr> <td colspan="2"><b>Total</b></td> <td colspan="2">3</td> </tr> </tbody> </table>	Dimension (diameter : $\Phi$ )	Acceptance (Q'ty)		A area	B area	$\Phi \leq 0.15$	Ignore		$0.15 < \Phi \leq 0.20$	2	Ignore	$0.20 < \Phi \leq 0.30$	2	$\Phi > 0.30$	0	<b>Total</b>	3		Dimension		Acceptance (Q'ty)		Length (L)	Width (W)	A area	B area	---	$W \leq 0.03$	Ignore	Ignore	$L \leq 5.0$	$0.03 < W \leq 0.05$	3	---	$W > 0.05$	As round type	<b>Total</b>		3		Minor
Dimension (diameter : $\Phi$ )	Acceptance (Q'ty)																																										
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07	<p>Polarizer Bubble</p>	<table border="1" data-bbox="542 1447 1334 1854"> <thead> <tr> <th rowspan="2">Dimension (diameter : <math>\Phi</math>)</th> <th colspan="2">Acceptance (Q'ty)</th> </tr> <tr> <th>A area</th> <th>B area</th> </tr> </thead> <tbody> <tr> <td><math>\Phi \leq 0.20</math></td> <td colspan="2">Ignore</td> </tr> <tr> <td><math>0.20 &lt; \Phi \leq 0.50</math></td> <td>3</td> <td rowspan="2">Ignore</td> </tr> <tr> <td><math>\Phi &gt; 0.50</math></td> <td>0</td> </tr> <tr> <td><b>Total</b></td> <td colspan="2">3</td> </tr> </tbody> </table>	Dimension (diameter : $\Phi$ )	Acceptance (Q'ty)		A area	B area	$\Phi \leq 0.20$	Ignore		$0.20 < \Phi \leq 0.50$	3	Ignore	$\Phi > 0.50$	0	<b>Total</b>	3		Minor																								
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		<p>8.1 General glass chip :</p> <p>8.1.1 Chip on panel surface and crack between panels:</p>  <table border="1" data-bbox="550 1433 1348 1724"> <thead> <tr> <th>X</th> <th>Y</th> <th>Z</th> </tr> </thead> <tbody> <tr> <td><math>\leq a</math></td> <td>Crack can't enter viewing area</td> <td><math>\leq 1/2 t</math></td> </tr> <tr> <td><math>\leq a</math></td> <td>Crack can't exceed the half of SP width.</td> <td><math>1/2 t &lt; Z \leq 2 t</math></td> </tr> </tbody> </table>		X	Y	Z	$\leq a$	Crack can't enter viewing area	$\leq 1/2 t$
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<p>8.2 Protrusion over terminal :</p> <p>8.2.1 Chip on electrode pad :</p>  <table border="1" data-bbox="571 1653 1347 1823"> <thead> <tr> <th></th> <th>X</th> <th>Y</th> <th>Z</th> </tr> </thead> <tbody> <tr> <td>Front</td> <td><math>\leq a</math></td> <td><math>\leq 1/2 W</math></td> <td><math>\leq t</math></td> </tr> <tr> <td>Back</td> <td><math>\leq a</math></td> <td><math>\leq W</math></td> <td><math>\leq 1/2 t</math></td> </tr> </tbody> </table>		X	Y	Z	Front	$\leq a$	$\leq 1/2 W$	$\leq t$	Back	$\leq a$	$\leq W$	$\leq 1/2 t$
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NO	Item	Criterion	Level
08	The crack of glass	<p>Symbols :</p> <p>X : The length of crack                      Y : The width of crack.            Z : The thickness of crack                W : terminal length            t : The thickness of glass                 a : LCD side length</p>	Minor
		<p>8.2.2 Non-conductive portion :</p>  <p>⊙ If the chipped area touches the ITO terminal, over 2/3 of the ITO must remain and be inspected according to electrode terminal specifications.</p> <p>8.2.3 Glass remain :</p> 	

**◆Specification For TFT-LCD Module Less Than 3.5" :**

(Ver.B01)

NO	Item	Criterion	Level
09	Backlight elements	9. 1 Backlight can't work normally.	Major
		9. 2 Backlight doesn't light or color is wrong.	Major
		9. 3 Illumination source flickers when lit.	Major
10	General appearance	10. 1 Pin type 、 quantity 、 dimension must match type in structure diagram.	Major
		10. 2 No short circuits in components on PCB or FPC .	Major
		10. 3 Parts on PCB or FPC must be the same as on the production characteristic chart .There should be no wrong parts , missing parts or excess parts.	Major
		10. 4 Product packaging must the same as specified on packaging specification sheet.	Minor
		10. 5 The folding and peeled off in polarizer are not acceptable.	Minor
		10. 6 The PCB or FPC between B/L assembled distance(PCB or FPC ) is $\leq 1.5$ mm.	Minor



## 5. PRECAUTION RELATING PRODUCT HANDLING

### 5.1 SAFETY

- 5.1.1 If the LCD panel breaks , be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes , please wash it off immediately by using soap and water.

### 5.2 HANDLING

- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module , be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So , please handle it very carefully ,do not touch , push or rub the exposed polarizing with anything harder than an HB pencil lead (glass , tweezers , etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth , as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands , this will stain the display area.
- 5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 5.2.8 To control temperature and time of soldering is  $320\pm 10^{\circ}\text{C}$  and 3-5 sec.
- 5.2.9 To avoid liquid (include organic solvent) stained on LCM .
- 5.2.10 Caution!( LCM products with Capacitive Touch Panel)  
Strong EMI-sources such as switch-mode power supplies (SMPS) can lead to touch malfunction (e.g. ghost-touches).  
Therefore, the touch needs to be thoroughly tested inside the target application.

### 5.3 STORAGE

- 5.3.1 Store the panel or module in a dark place where the temperature is  $25^{\circ}\text{C} \pm 5^{\circ}\text{C}$  and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush , shake , or jolt the module.

### 5.4 TERMS OF WARRANTY

- 5.4.1 Applicable warrant period  
The period is within thirteen months since the date of shipping out under normal using and storage conditions.
- 5.4.2 Unaccepted responsibility  
This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment , we cannot take responsibility if the product is used in

nuclear power control equipment , aerospace equipment , fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.



